

I-DRIVE 5, 28.8 WATT LED DRIVER

Dual Mode: 90V, 0-320mA Day; 90V, 0-15mA NVIS

GENERAL DESCRIPTION

The ACI-2907 provides a single board LED Driver solution for the powering of a LED backlight consisting of two types of LEDs, High Bright "Day" LEDs, and IR filtered "NVIS" LEDs.

This driver operates as a Dual Mode constant current LED Driver. In "Day" mode, the driver supplies power out of the Day channel (CON3, pins 1 and 5), and in the "NVIS" mode, out of the NVIS channel (CON3, pins 1 and 3). Mode selection is accomplished via a signal provided by the user on pin 11 of CON1.

The driver is capable of driving series connected LEDs with a forward voltage drop up to 90V, and operates over an input voltage range from +10.5V to +18V DC.

MECHANICAL/ENVIRONMENTAL

Weight = 35.7 grams

Altitude = 35,000 ft max.

Humidity < 95% non-condensing

Size (L x W x H) = 3.25 IN x 2.00 IN x 0.47 IN

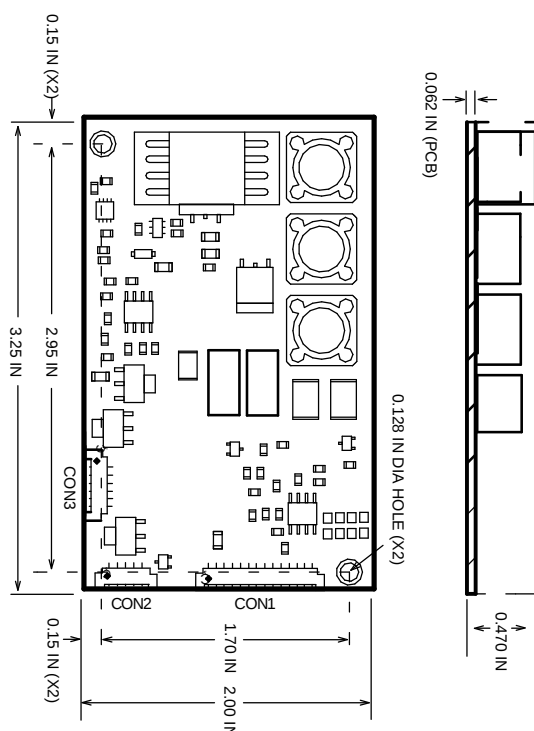
PCB thickness = 0.062 IN

Mounting Holes = 0.128 IN diameter (X2)

Input Power & Control Connector = CON1

LED Output Connector = CON3

RoHS Compliant



INPUT CONNECTOR

CON1

MOLEX 53261-1471

PIN# FUNCTION

1	+10.5 to +18V (PWR)
2	+10.5 to +18V (PWR)
3	+10.5 to +18V (PWR)
4	+10.5 to +18V (PWR)
5	GND (PWR)
6	GND (PWR)
7	GND (PWR)
8	GND (PWR)
9	ENABLE
10	ICNTL
11	DAY/NVIS MODE SELECT
12	TBD (FOR FUTURE USE)
13	+5VDC REF OUT
14	GND (SIGNAL)

OUTPUT CONNECTOR

CON3

MOLEX 53261-0671

PIN# FUNCTION

1	+LED (ANODE)
2	+LED (ANODE)
3	-LED (NVIS CATHODE)
4	-LED (NVIS CATHODE)
5	-LED (DAY CATHODE)
6	-LED (DAY CATHODE)

(CON2 NOT USED)

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
Vin	Supply Voltage (Referenced to Ground)	-0.7 to 20	Vdc
Vip	Voltage applied to any Input Pin (Referenced to Ground)	-0.7 to 5.7	Vdc
Iop	Current sourced or sinked from any Output Control Pin	+/- 10	mAdc
Pin	Input Power (DC Input Voltage x DC Input Current)	38	W
Top	Operating Temperature (Still air ambient around Driver)	-30 to +70	°C
Tstg	Storage Temperature	-40 to +105	°C

*Maximum Ratings are those values beyond which damage to the driver may occur.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
Vin	Supply Voltage (Referenced to Ground)	10.5	18.0	Vdc
Vf	Series Connected Cumulative LED Forward-Drop Voltage (DAY)	36	90	Vdc
Vf	Series Connected Cumulative LED Forward-Drop Voltage (NVIS)	36	90	Vdc
Icntl	LED Intensity Control Voltage	0.25	4.75	Vdc

ELECTRICAL CHARACTERISTICS (Part 1 of 2)

Vin = +12V, Icntl = +4.75V, Enable = +5V, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Nom	Max	Unit
OCV	Open Circuit Voltage	No Load	133	140	147	Vdc
ENoff	Enable Control, Unit Off	Pin 9			0.5	Vdc
ENon	Enable Control, Unit ON	Pin 9	2.0			Vdc
Ms	Mode Select, Day Mode	Pin 11	2.0			Vdc
Ms	Mode Select, NVIS Mode	Pin 11			0.5	Vdc
Msd	Mode Select Delay (Time for output channel to switch to alternate mode/channel)			0.2		sec
+5Vref	+5V Output Reference Voltage	1K Ohm Load to Ground (Pin 12)	4.75	5.0	5.25	Vdc
Iind	Input Current Draw (Disabled)	Enable (Pin 9) = 0V		8		mA
Eff	Electrical Efficiency			87		%

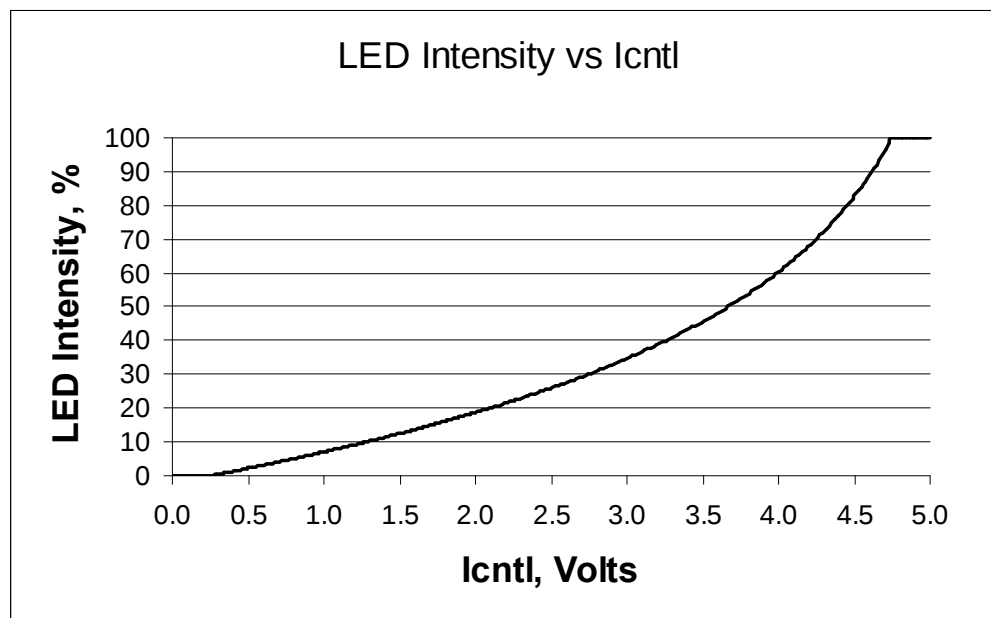
Electricals continued on page 3.

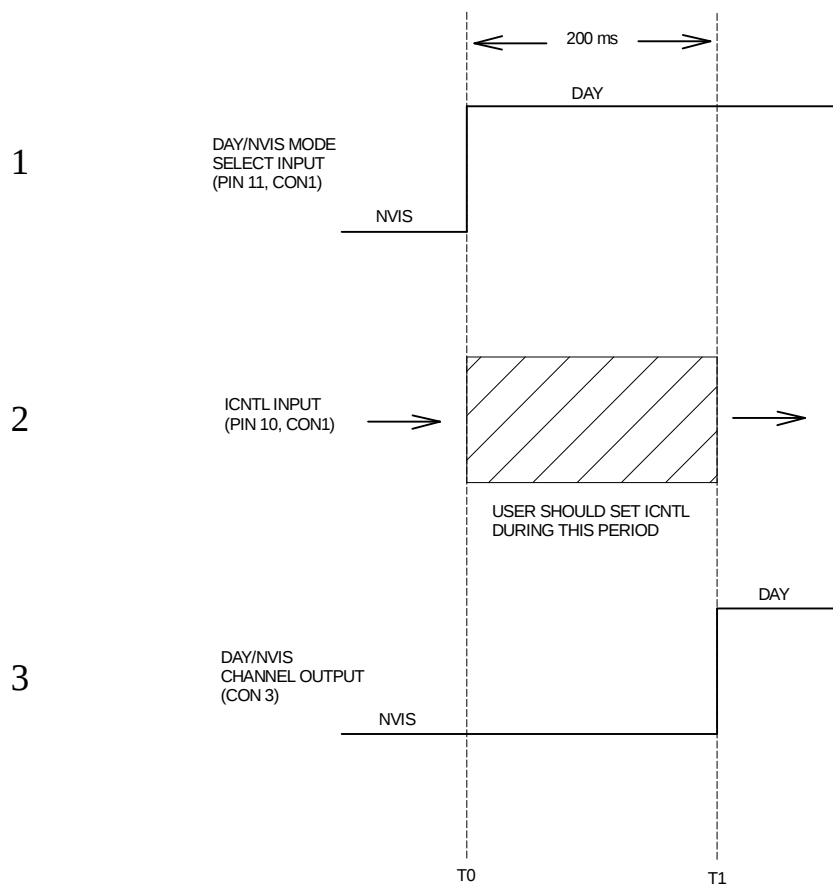
ELECTRICAL CHARACTERISTICS (Part 2 of 2)

Vin = +12V, Icntl = +4.75V, Enable = +5V, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Nom	Max	Unit
Day Mode, Ms = 5V						
Iout	Output Current		304	320	336	mAdc
Iadj	Nominal Output Current Adjustment Range	Icntl(Pin 10) ≤ +0.25V	0			mAdc
		Icntl(Pin 10) ≥ +4.75V			320	mAdc
Iin	Input Current Draw	Vf = 90V		2.8		Adc
NVIS Mode, Ms = 0V						
Iout	Output Current		14	15	16	mAdc
Iadj	Nominal Output Current Adjustment Range	Icntl(Pin 10) ≤ +0.25V	0			mAdc
		Icntl(Pin 10) ≥ +4.75V			15	mAdc
Iin	Input Current Draw	Vf = 90V		0.13		Adc

BRIGHTNESS CURVE





TIMING DIAGRAM – Example of Switching from NVIS to Day Mode

When user wants to switch modes, do the following:

1. Change to the desired mode, via Pin 11 on CON1 (This is shown to occur at T0).
2. Update Icntl setting, Pin 10 on CON1, within 200ms of mode change.
3. 200ms after mode change, the output channel will switch, and the new Icntl setting will appear (this is shown to occur at T1).