

DDS-SA11110

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NEC NEC LCD Technologies, Ltd.**TFT COLOR LCD MODULE****NL2432HC22-44B****8.9cm (3.5 Type)
QVGA****SPECIFICATIONS**

(1st edition)

Signature of writer

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Oct. 1, 2008

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Oct. 1, 2008

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INTRODUCTION

• WARRANTY

NEC LCD Technologies, Ltd. (hereinafter called "NEC") warrants that this product meets the product specifications set forth in this document. If this product under normal operation is found to be non-conforming to the product specifications, and such non-conformance is promptly notified to NEC within one (1) year after the delivery date, and further such non-conformance is solely attributable to NEC, NEC shall replace the non-conforming product free of charge. However, this warranty does not apply to any non-conformance resulting from any one of the following:

- 1) Unauthorized or improper repair, maintenance and modification
- 2) Operation or usage outside of specifications and instructions or warnings given by NEC
- 3) Any other causes attributable to customers

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• HANDLING OF DOUBTFUL POINTS

Any question arising out of, or in connection with, this SPECIFICATION or any matter not stipulated herein will be settled each time upon consultation between both parties.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL2432HC22-44B is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array, touch panel (T/P) and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a controller, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

- PDAs

1.3 FEATURES

- Adoption of SR-NLT (Super-Reflective Natural Light TFT) (Transflective type)
- Backlight and touch panel attached
- High luminance
- High contrast
- Including LCD controller and power supply
- 6-bit digital RGB signals
- Thick touch panel
- Compliance with the European RoHS directive (2002/95/EC)

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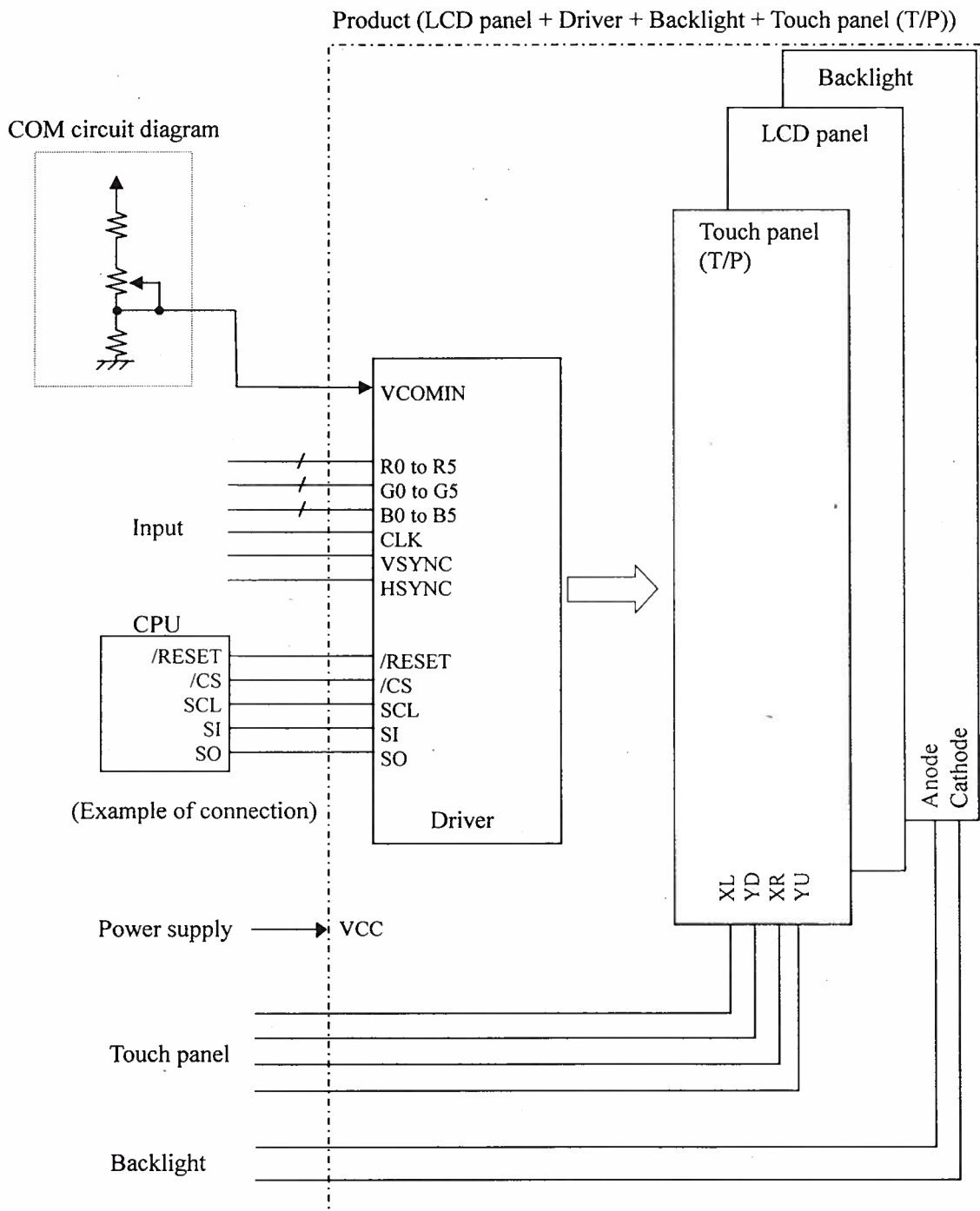
2. GENERAL SPECIFICATIONS

<i>Display area</i>	53.64 (H) × 71.52 (V) mm
<i>Diagonal size of display</i>	8.9 cm (3.5 inches)
<i>Drive system</i>	a-Si TFT active matrix
<i>Display color</i>	262,144 colors
<i>Pixel</i>	240 (H) × 320 (V) pixels
<i>Pixel arrangement</i>	RGB (Red dot, Green dot, Blue dot) vertical stripe
<i>Dot pitch</i>	0.0745 (H) × 0.2235 (V) mm
<i>Pixel pitch</i>	0.2235 (H) × 0.2235 (V) mm
<i>Module size</i>	63.5 (H) × 85.0 (V) × 4.6 (D) mm (typ.)
<i>Weight</i>	49g (typ.)
<i>Touch panel surface</i>	Clear
<i>Touch panel pencil-hardness</i>	3H (min.) [by JIS K5400]
<i>Luminance</i>	At IL= 20mA, with Touch panel 200cd/m ² (typ.)
<i>Reflection ratio</i>	With Touch panel 15% (typ.)
<i>Contrast ratio</i>	At transmissive mode, IL= 20mA, with Touch panel 150:1 (typ.)
	At reflective mode, with Touch panel 15:1 (typ.)
<i>Response time</i>	Ton+Toff (10%←→90%) 30 ms (typ., at transmissive mode) 16 ms (typ., at reflective mode)
<i>Signal system</i>	6-bit digital signals for data of RGB colors, Dot clock (CLK), Horizontal synchronous signal (HSYNC), Vertical synchronous signal (VSYNC) Serial interface (SPI correspondence) (/CS, SCL, SI, SO)
<i>Supply voltage</i>	VCC: 3.0V (typ.)
<i>Power consumption</i>	LCD panel: 50 mW (typ.) Backlight: 384mW (typ., at IL=20mA)

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3. BLOCK DIAGRAM

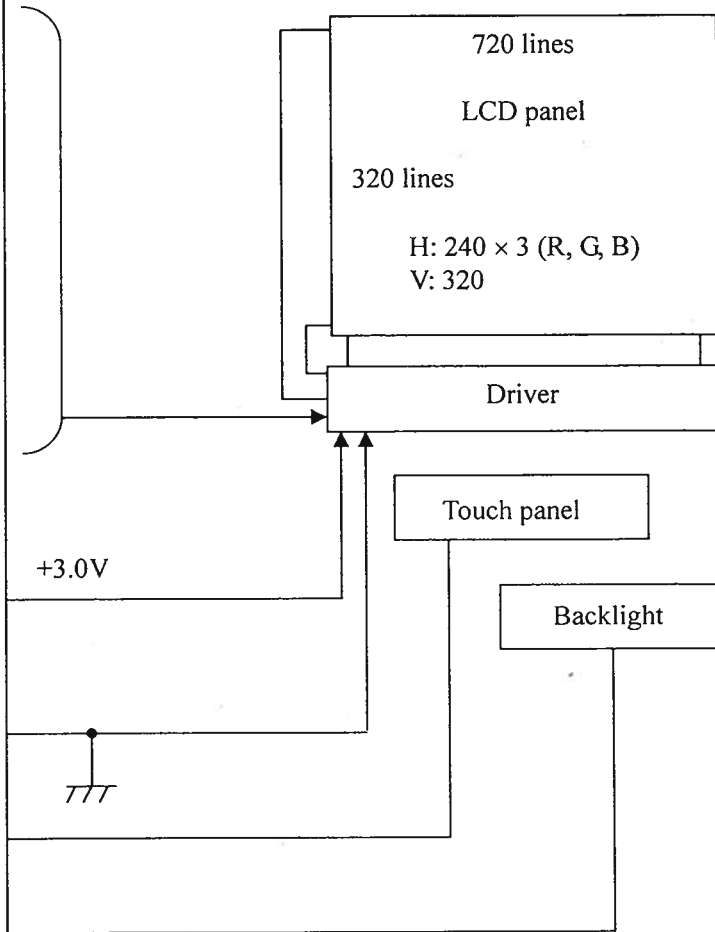


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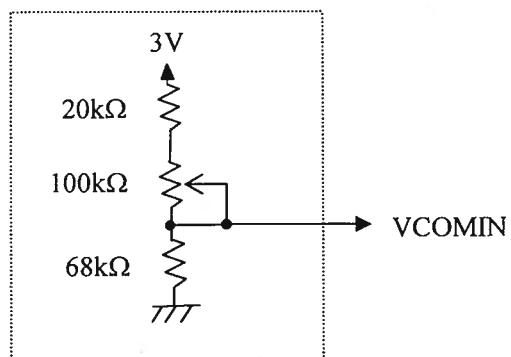
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FPC I/F

PinNo.	Symbol
13 to 18	B0 to B5
19 to 24	G0 to G5
25 to 30	R0 to R5
10	VSYNC
9	HSYNC
11	CLK
8	/RESET
35	/CS
32	SCL
33	SI
34	SO
36	VCOMIN
3, 4, 5, 6	VCC
1, 2, 7, 12, 31, 38, 43	GND
39, 40, 41, 42	XL, YD XR, YU
44	ANODE
45	CATHODE



Reference design of COM circuit



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4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification	Unit
Module size	63.5 ± 0.3 (W) $\times$ 85.0 ± 0.3 (H) $\times$ 4.6 ± 0.2 (D) Note1	mm
Display area	53.64 (H) $\times$ 71.52 (V) Note2	mm
Weight	49 (typ.), 51 (max.)	g

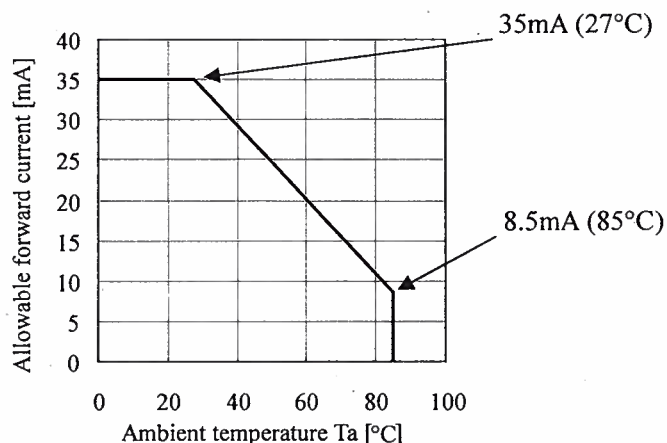
Note1: Excluding FPC

Note2: See "12. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit	Remarks
Supply voltage	VCC	-0.5 to +6.0	V	Ta= 25°C
Logic input voltage	VI	-0.5 to VCC+0.5	V	Logic signals
Backlight	Reverse voltage	VR	≤ 30	Ta= 25°C
	Power dissipation	PD	≤ 738	
	Forward current	IL	Note1	
	Pulse forward current	IFP	100	Pulse width ≤ 10ms, Duty ≤ 1/10
Storage temperature	Tst	-30 to +80	°C	-
Operating temperature	Top	-20 to +70		Product surface Note2
Relative humidity Note3	RH	≤ 95	%	Ta ≤ 40°C
		≤ 85		40°C < Ta ≤ 50°C
		≤ 70		50°C < Ta ≤ 55°C
Absolute humidity Note3	AH	≤ 73 Note4	g/m ³	Ta > 55°C
Storage altitude		≤ 13,600	m	-30°C ≤ Ta ≤ 80°C
Operating altitude		≤ 4,850	m	-20°C ≤ Ta ≤ 70°C

Note1: Allowable forward current



Note2: Measured at display area

Note3: No condensation

Note4: Water amount at Ta= 55°C and RH= 70%

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4.3 ELECTRICAL CHARACTERISTICS

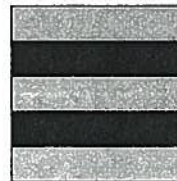
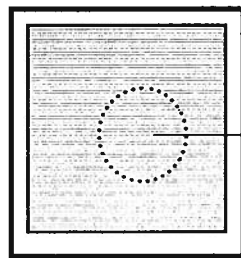
(1) Logic/ LCD driving

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Logic supply voltage	VCC	2.85	3.0	3.15	V	-
Logic input high voltage	VIH	0.8VCC	-	VCC	V	Logic signal
Logic input low voltage	VIL	0	-	0.2VCC	V	
COM/H voltage	COM/H	-	1.8	-	V	at VCC= 3.0V Note1
VCC supply current	ICC	-	16.5	26	mA	Normal mode at VCC= 3.0V Note2
	ICCs	-	0.2	-		Stand-by mode at VCC= 3.0V Note2

Note1: The optimum value for COM/H is in the range of 1.3 V to 2.3 V.

Recommended adjustment display for COM/H



Half tone display (32/63 gray scale)

Full black display (0/63 gray scale)

Half tone display (32/63 gray scale)

Full black display (0/63 gray scale)

Half tone display (32/63 gray scale)

Note2: CLK= 5.0MHz, HSYNC= 19.53kHz, VSYNC= 60Hz,
Checkered flag pattern (by EIAJ ED-2522)

(2) Backlight

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward Current	IL	-	20	22	mA	-
Forward Voltage	VL	-	19.2	21.0	V	at IL= 20mA

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(3) Touch panel

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Touch panel input voltage	Vtp	-	-	5.5	V	-
Resistor between pins(XL-XR)	Rx	190	-	500	Ω	-
Resistor between pins(YU-YD)	Ry	140	-	540	Ω	-
Line linearity (X direction)	Xlin	-	-	1.5	%	Note1
Line linearity (Y direction)	Ylin	-	-	1.5	%	Note1
Insulation resistance	Rins	20	-	-	M Ω	at DC 25V
Static Capacitance	Ctp	-	-	100	nF	-
Chattering	Chat	-	-	10	ms	Note1
Operation starting force	Ost	-	-	0.78 80	N gf	Note1, Note2
Surface hardness	Hs	3	-	-	H	Pencil hardness
Point hitting life	Lhp	1,000,000	-	-	times	Polyacetal stylus pen: R0.8mm Load: 2.45N(250gf)
	Lhr	1,000,000	-	-	times	Silicon rubber: R8mm, Hardness 60° Load: 2.94N(300gf)
Line writing life	Lwl	50,000	-	-	times	Polyacetal stylus pen: R0.8mm Load: 2.45N(250gf), 35mm

Note1: Input methods are a Finger or R0.8mm Polyacetal Stylus Pen

Note2: Test condition

Resistance between X and Y axis must be 2k Ω or less, and the test voltage is 5V DC.

4.4 SETTING OF THE INTERNAL REGISTER

Initial setting of the internal Register is undefined data. So the Register Data must be written in the Register, after initialization by the /RESET pin. The Register Data can be written and read from serial interface pins (/CS, SCL, SI and SO). The setting method is as follows.

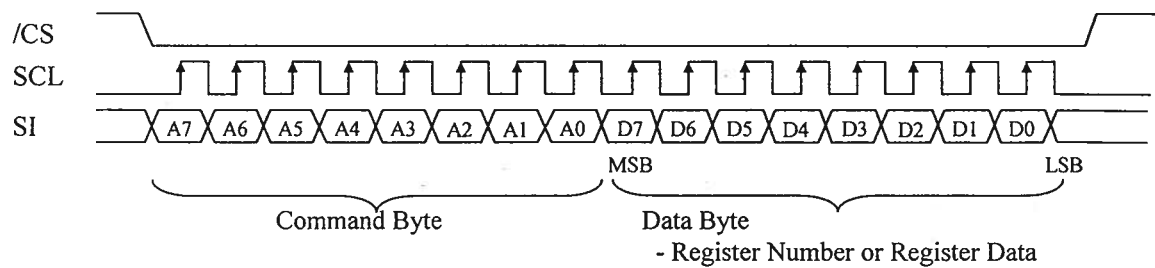
(1) Command Byte Function

Bits	Functions	Discription
A7	-	-
A6	-	-
A5	Read / Write	0:Write 1:Read
A4	-	-
A3	-	-
A2	-	-
A1	-	-
A0	Register Number / Data	0:Register Number 1:Register Data

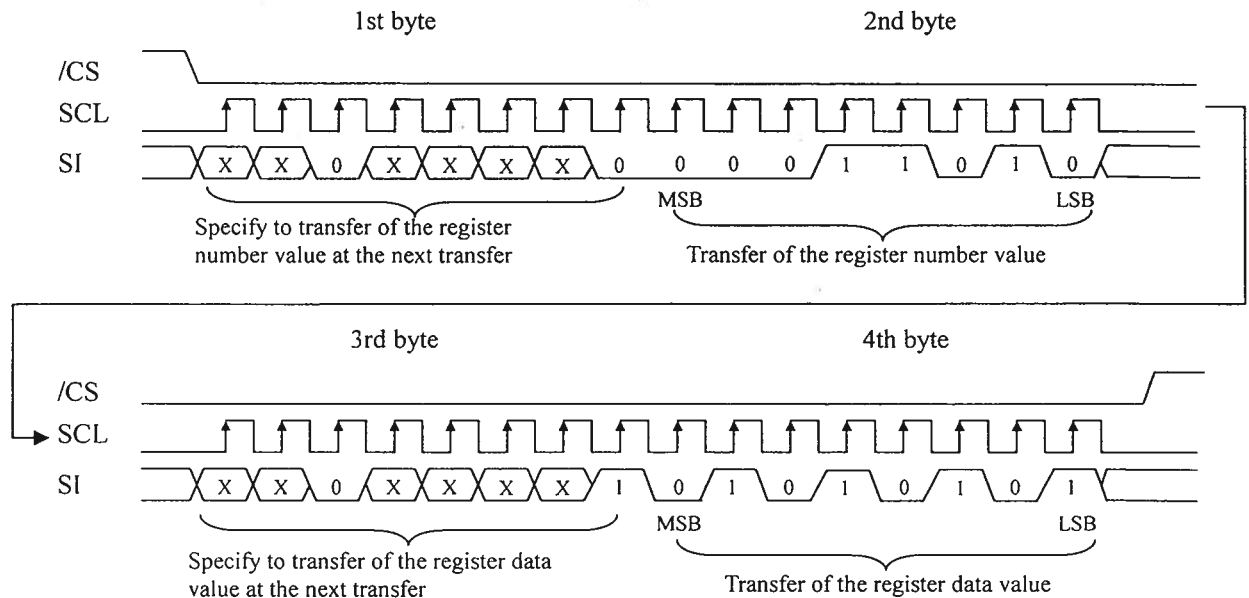
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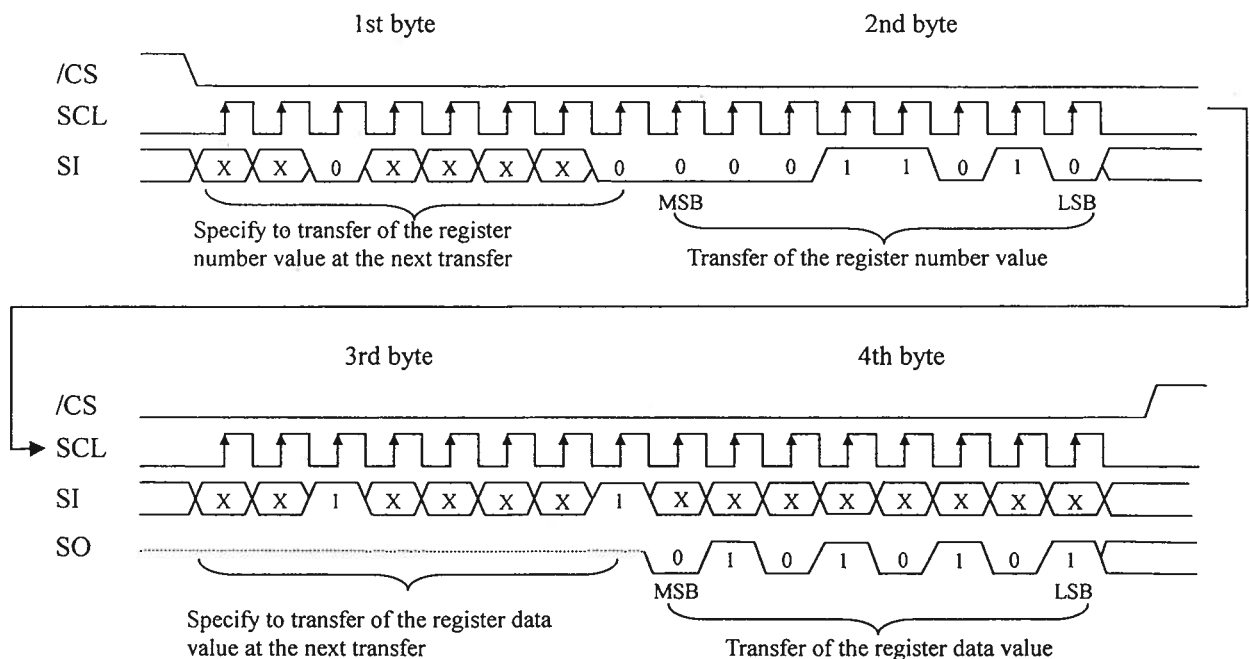
(2) Timing chart



Ex) When data 55h is written to Register R26 (R1Ah)



Ex) When data 55h is read to Register R26 (R1Ah)



Note1: During 32-bit transfer of the Register Data, /CS pin (Pin No.35) must be maintained active.

Note2: "X" is set in accordance with the usage conditions.

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(3) Command sequence

①Power On

Sequence	Register Number	Data	Comment	Sequence	Register Number	Data	Comment
1	Power On			26	R76	01h	-
2	1ms min. wait.			27	R77	01h	-
3	Reset by the /RESET pin (Pin No. 8).			28	R80	00h	-
4	1ms min. wait after /RESET↑.			29	R81	00h	-
5	R3	01h	-	30	R82	2Eh	-
6	R1	00h	-	31	R83	C4h	-
7	R100	0Fh	-	32	R86	15h	-
8	R101	3Fh	-	33	R87	EDh	-
9	R102	3Fh	-	34	R95	3Fh	-
10	R103	00h	-	35	R96	22h	-
11	R104	00h	-	36	R25	76h	-
12	R105	30h	-	37	R26	54h	-
13	R106	04h	-	38	R27	67h	-
14	R107	37h	-	39	R28	60h	-
15	R108	17h	-	40	R29	04h	-
16	R109	00h	-	41	R30	1Ch	-
17	R110	40h	-	42	R31	A9h	-
18	R111	30h	-	43	R32	00h	-
19	R112	04h	-	44	R33	20h	-
20	R113	37h	-	45	R24	77h	-
21	R114	17h	-	46	30 μ s min. wait.		
22	R115	00h	-	47	Data input start		
23	R116	40h	-	48	R59	01h	-
24	R2	40h	-	49	20 ms min. wait.		
25	R75	04h	-	50	R0	00h	-

②Power Off

Sequence	Register Number	Data	Comment
1	R0	08h	-
2	25 ms min. wait.		
3	R24	00h	-
4	20 ms min. wait.		
5	R1	08h	-
6	Data Off		
7	Power Off		

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③ Standby

Sequence	Register Number	Data	Comment
1	R0	08h	-
2	25 ms min. wait.		
3	R24	00h	-
4	20 ms min. wait.		
5	R1	08h	-

④ Wake Up

Sequence	Register Number	Data	Comment
1	R1	00h	-
2	R24	FFh	-
3	30 μ s min. wait.		
4	R0	00h	-

Note1: Be sure to perform reset by the /RESET pin (Pin No. 8) every power-on

Note2: Write the Register Data every power-on, because the data are not stored in the product.

Note3: Due to influence such as static electricity from the outside, data in the register may transform. Data is recommended to be written in the register regularly.

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4.5 INTERFACE PIN CONNECTIONS

CN1 (FPC)

Adaptable socket: FH23-45S-0.3SHW(05) (Hirose Electric Co., Ltd.(HRS))

Pin No.	Symbols	Functions	Pin No.	Symbols	Functions
1	GND	Ground Note1	25	R0	Red data (LSB)
2	GND	Ground Note1	26	R1	Red data
3	VCC	Power supply Note1	27	R2	Red data
4	VCC		28	R3	Red data
5	VCC		29	R4	Red data
6	VCC		30	R5	Red data (MSB)
7	GND	Ground Note1	31	GND	Ground Note1
8	/RESET	Reset	32	SCL	Serial clock
9	HSYNC	Horizontal synchronous signal	33	SI	Serial input
10	VSNC	Vertical synchronous signal	34	SO	Serial out put
11	CLK	Dot clock	35	/CS	Chip selection
12	GND	Ground Note1	36	VCOMIN	COM high voltage input
13	B0	Blue data (LSB)	37	N.C.	Keep this pin Open.
14	B1	Blue data	38	GND	Ground Note1
15	B2	Blue data	39	XL	Horizontal terminal (Left side)
16	B3	Blue data	40	YD	Vertical terminal (Down side)
17	B4	Blue data	41	XR	Horizontal terminal (Right side)
18	B5	Blue data (MSB)	42	YU	Vertical terminal (Up side)
19	G0	Green data (LSB)	43	GND	Ground Note1
20	G1	Green data	44	ANODE	LED voltage (Anode)
21	G2	Green data	45	CATHODE	LED voltage (Cathode)
22	G3	Green data			
23	G4	Green data			
24	G5	Green data (MSB)			

Note1: All GND terminals should be used without any non-connected lines.

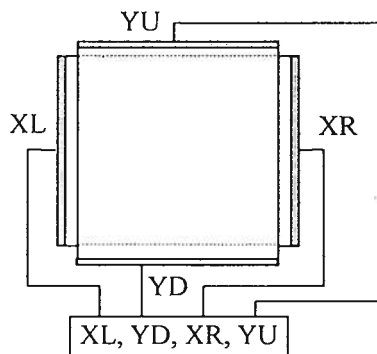
Note2: Do not fold the FPC. When folding the FPC, pattern disconnection may occur. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.

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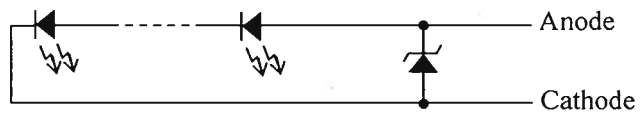
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Description of terminals

Terminals	Description
/RESET	When /RESET is L, an internal reset is performed. The reset operation is executed at the /RESET signal level. Be sure to perform reset via this pin at power application.
/CS	This pin is used for chip select signals. When /CS= L, the chip is active and can perform data I/O operations including command and data I/O.
SCL	This pin is clock input of serial interface.
SI	This pin is data input of serial interface.
SO	This pin is data output of serial interface.
VCOMIN	This pin is the Common high voltage. The voltage needs to be adjusted. The details are explained the above. See "3 BLOCK DIAGRAM - Reference design of COM circuit".
YU,XR,YD,XL	Refer to the below "Circuits of touch panel".
ANODE, CATHODE	Refer to the below "Circuits of backlight".



Circuits of touch panel



Circuits of backlight

4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display in equivalent to 262,144 colors in 64 gray scales. Also the relation between display colors and input data signals is as the following table.

Display colors		Data signal (0: Low level, 1: High level)																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑																		
	↓																		
	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Green gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	↑																		
	↓																		
	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
Blue gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑																		
	↓																		
	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

4.7 DISPLAY POSITIONS

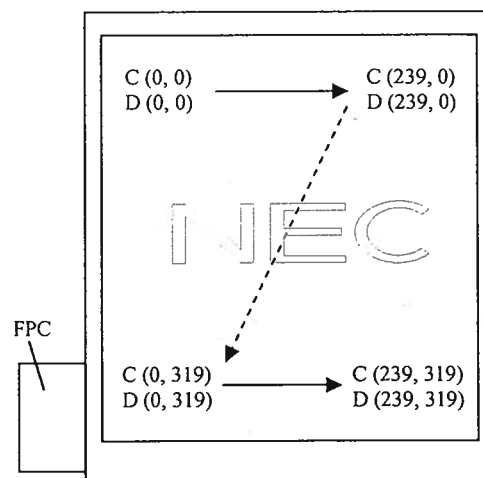
The following table is the coordinates per pixel (See figure of "4.8 SCANNING DIRECTIONS".).

C(0, 0)			C(1, 0)		
R	G	B	R	G	B

C(0, 0)	C(1, 0)	...	C(X, 0)	...	C(238, 0)	C(239, 0)
C(0, 1)	C(1, 1)	...	C(X, 1)	...	C(238, 1)	C(239, 1)
⋮	⋮	⋮	⋮	⋮	⋮	⋮
C(0, Y)	C(1, Y)	...	C(X, Y)	...	C(238, Y)	C(239, Y)
⋮	⋮	⋮	⋮	⋮	⋮	⋮
C(0, 318)	C(1, 318)	...	C(X, 318)	...	C(238, 318)	C(239, 318)
C(0, 319)	C(1, 319)	...	C(X, 319)	...	C(238, 319)	C(239, 319)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.



Note1: Meaning of C(X, Y) and D(X, Y)

C(X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

D(X, Y): The data number of input signal for LCD panel

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4.9 INPUT SIGNAL TIMINGS

4.9.1 RGB interface (Ta= 25°C, VCC= 3.0V)

(1) Timing characteristics

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	4.81	5.0	5.12	MHz	200ns (typ.)
	Duty		tcd	0.4	0.5	0.6	-	-
	Rise time, Fall time		tcrf	-	-	15	ns	-
DATA (R0-5) (G0-5) (B0-5)	CLK-DATA	Setup time	tds	15	-	-	ns	-
		Hold time	tdh	15	-	-	ns	
	Rise time, Fall time		tdrf	-	-	15	ns	
HSYNC	Cycle		th	50.0	51.2	53.2	μs	19.53kHz (typ.)
				256			CLK	-
	Display period		thd	240			CLK	
	Front-porch		thf	4			CLK	
	Pulse width		thp	2	8	-	CLK	
	Back-porch		thb	4			CLK	
	CLK-HSYNC	Setup time	ths	15	-	-	ns	
		Hold time	thh	15	-	-	ns	
	Rise time, Fall time		thrf	-	-	15	ns	
VSYNC	Cycle		tv	16.2	16.59	17.24	ms	60Hz (typ.)
				324			H	-
	Display period		tvd	320			H	
	Front-porch		tvf	1			H	
	Pulse width		tvp	1	2	-	H	
	Back-porch		tvb	1			H	
	VSYNC-HSYNC timing		tvh	15	-	-	ns	
	Rise time, Fall time		tvrf	-	-	15	ns	

Note1: Definition of parameters is as follows.

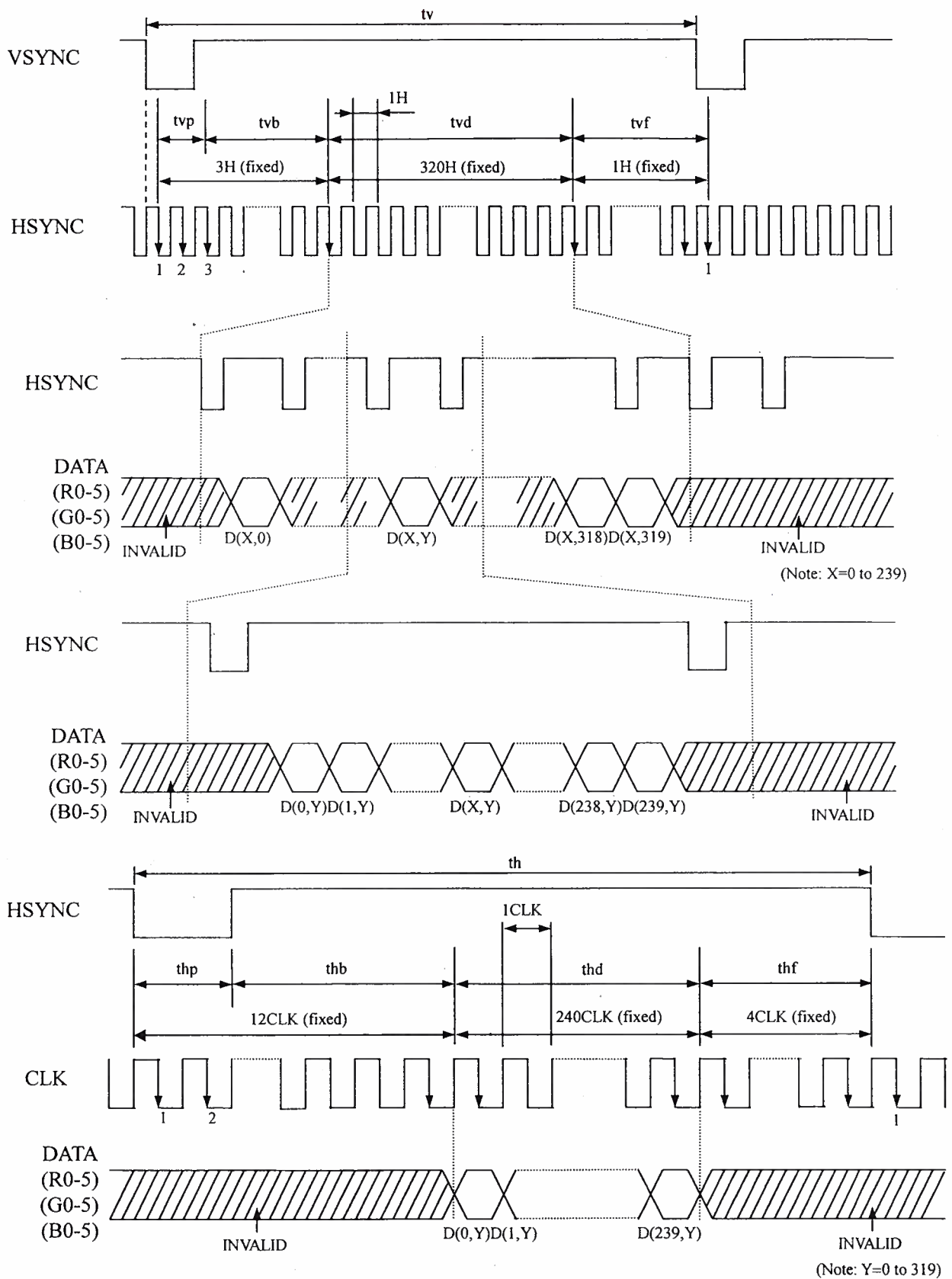
tc= 1CLK, tcd= tch/tc, th= 1H

Note2: All parameters should be kept within the specified range.

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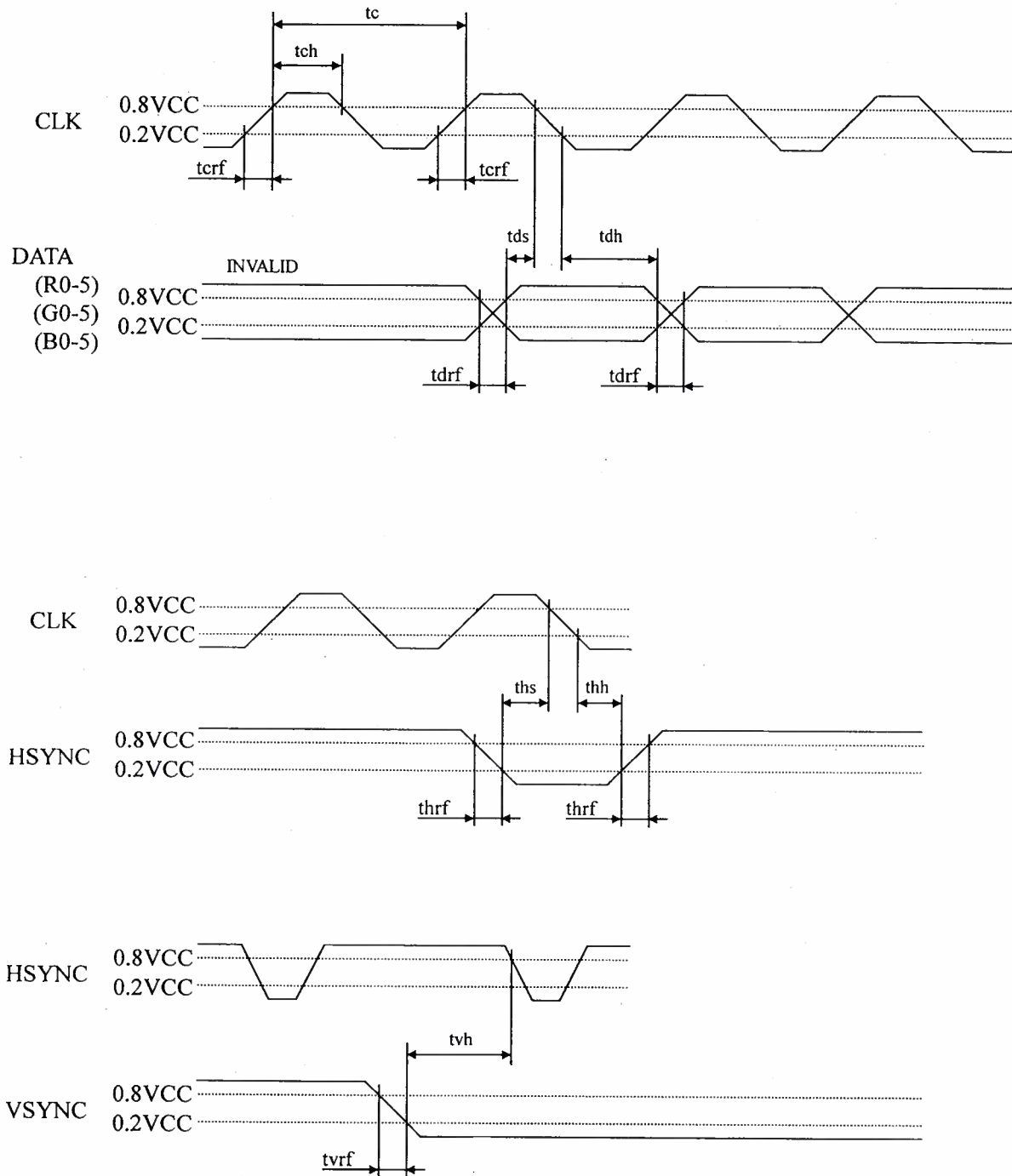
(2) Input signal timing chart



Note1: Unless otherwise specified, the input level is defined to be $V_{IH} = 0.8V_{CC}$, $V_{IL} = 0.2V_{CC}$.

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Note1: Unless otherwise specified, the input level is defined to be $V_{IH} = 0.8V_{CC}$, $V_{IL} = 0.2V_{CC}$.

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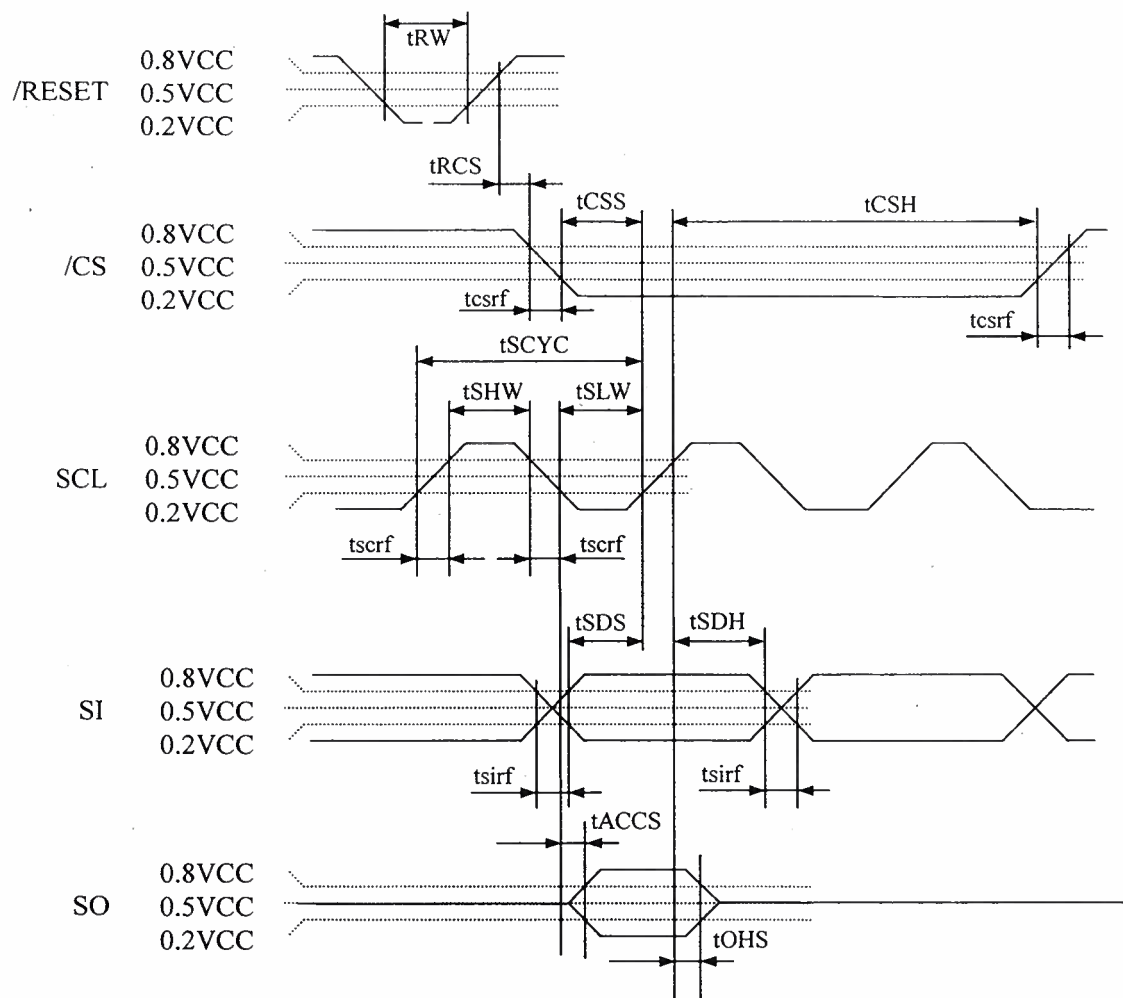
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4.9.2 Serial interface (Ta= 25°C, VCC= 3.0V)

(1) Timing characteristics

Parameter	Symbol	Condition	min.	typ.	max.	Unit	Remarks
Serial clock cycle	tSCYC	READ	450	-	-	ns	-
		WRITE	100	-	-	ns	-
SCL high level pulse width	tSHW	READ	210	-	-	ns	-
		WRITE	40	-	-	ns	-
SCL low level pulse width	tSLW	READ	210	-	-	ns	-
		WRITE	40	-	-	ns	-
/CS rise time, fall time	tcsrf	/CS	-	-	15	ns	-
SCL rise time, fall time	tscrf	SCL	-	-	15	ns	-
SI rise time, fall time	tsirf	SI	-	-	15	ns	-
/CS setup time	tCSS	/CS	50	-	-	ns	-
/CS hold time	tCSH	/CS	30	-	-	ns	-
Data setup time	tSDS	SI	30	-	-	ns	-
Data hold time	tSDH	SI	15	-	-	ns	-
Reset pulse width	tRW	/RESET	2	-	-	μs	-
/RESET↑ to /CS time	tRCS	/RESET↑ to /CS	1	-	-	ms	-
Access time	tACCS	SO	-	-	180	ns	-
Output disable time	tOHS	SO	-	-	100	ns	-

Note1: All parameters should be kept within the specified range.

Note2: Unless otherwise specified, the input level is defined to be $V_{IH} = 0.8VCC$, $V_{IL} = 0.2VCC$.

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4.10 OPTICAL CHARACTERISTICS

<Backlight turning OFF>

(Note1, Note3, Note4)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Reflection ratio	White, at center	RE	8	15	-	%	Note6
Contrast ratio	White/Black, at center	CR	10	15	-	-	Note7

Reference data

(Note1, Note3, Note4)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Chromaticity coordinates	White	Wx	-	0.32	-	-	Note8
		Wy	-	0.34	-	-	
Color gamut	at center, against NTSC color space	C	-	5	-	%	
Response time	White to black	Ton	-	7	14	ms	Note9 Note10
	Black to white	Toff	-	9	18		

<Backlight turning ON>

(Note2, Note3, Note5)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Luminance	White at center $\theta R = 0^\circ, \theta L = 0^\circ, \theta U = 0^\circ, \theta D = 0^\circ$	L	150	200	-	cd/m ²	-
Contrast ratio	White/Black at center $\theta R = 0^\circ, \theta L = 0^\circ, \theta U = 0^\circ, \theta D = 0^\circ$	CR	75	150	-	-	Note7
Luminance uniformity	White $\theta R = 0^\circ, \theta L = 0^\circ, \theta U = 0^\circ, \theta D = 0^\circ$ Maximum luminance: 100%	LU	60	70	-	%	Note11

Reference data

(Note2, Note3, Note5)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Chromaticity coordinates	White	Wx	0.25	0.30	0.35	-	Note8
		Wy	0.27	0.32	0.37	-	
Color gamut	$\theta R = 0^\circ, \theta L = 0^\circ, \theta U = 0^\circ, \theta D = 0^\circ$ at center, against NTSC color space	C	-	40	-	%	
Response time	White to black	Ton	-	7	14	ms	Note9 Note10
	Black to white	Toff	-	23	46		
Viewing angle	Right	θR	-	30	-	°	-
	Left	θL	-	30	-	°	
	Up	θU	-	30	-	°	
	Down	θD	-	35	-	°	

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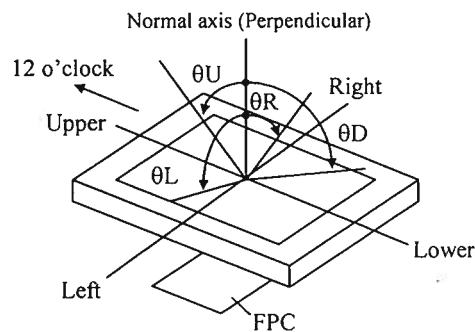
Note1: Measurement conditions are as follows.

$T_a = 25\text{ }^{\circ}\text{C}$, $V_{CC} = 3.0\text{V}$, with touch panel

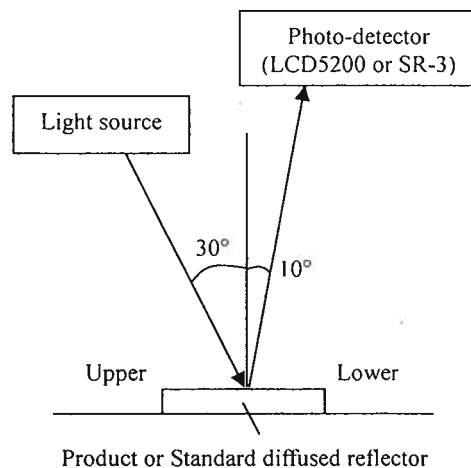
Note2: Measurement conditions are as follows.

$T_a = 25\text{ }^{\circ}\text{C}$, $V_{CC} = 3.0\text{V}$, $I_L = 20\text{mA}$, with touch panel

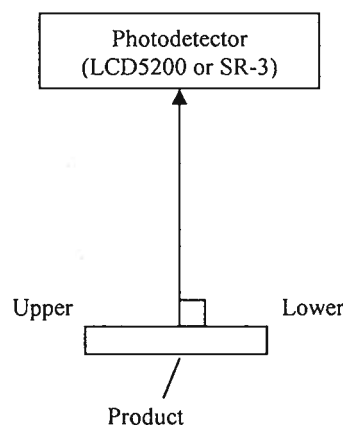
Note3: Definition of viewing angles



Note4: In reflective mode (Backlight turning OFF), Reflection ratio, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



Note5: In transmissive mode (Backlight turning ON), Luminance, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



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Note6: Definitions of reflection ratio

The reflection ratio is calculated by using the following formula.

$$\text{Reflection (RE)} = \frac{\text{Luminance of reflected light at white screen}}{\text{Luminance of standard diffused reflector}} \times 100$$

Note7: Definition of contrast ratio

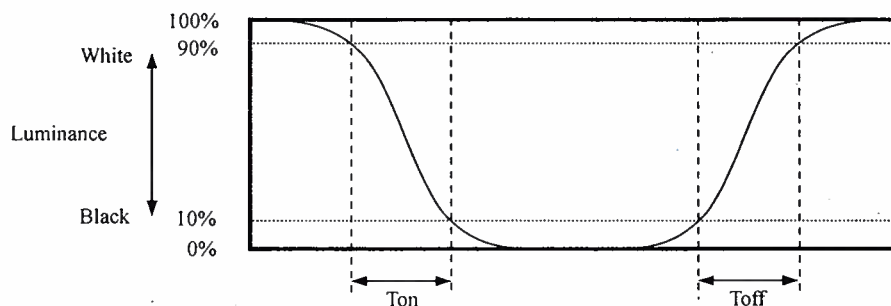
The contrast ratio is calculated by using the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance of white screen}}{\text{Luminance of black screen}}$$

Note8: The White chromaticity coordinates are deviated by the LED deviation in addition to color filter deviation.

Note9: Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).

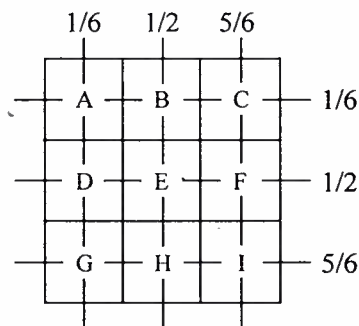


Note10: Product surface temperature: Top= 25°C

Note11: Definition of luminance uniformity

Luminance uniformity is calculated by using the following formula.

$$\text{Luminance uniformity (LU)} = \frac{\text{Minimum luminance from A to I}}{\text{Maximum luminance from A to I}} \times 100$$



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5. DEFECT SPECIFICATIONS

a) Inspection conditions are as follows.

Distance: The distance between the inspector's eye and the LCD panel is 20 to 35cm.

Temperature: $25 \pm 5^{\circ}\text{C}$

Viewing angle and illumination

Display specifications: $0^{\circ} \leq \theta_R \leq 20^{\circ}$, $0^{\circ} \leq \theta_L \leq 20^{\circ}$, $0^{\circ} \leq \theta_U \leq 20^{\circ}$, $0^{\circ} \leq \theta_D \leq 20^{\circ}$
 60lx at a LCD surface
 Backlight On (IL= 20mA)

Appearance specifications: $0^{\circ} \leq \theta_R \leq 45^{\circ}$, $0^{\circ} \leq \theta_L \leq 45^{\circ}$, $0^{\circ} \leq \theta_U \leq 45^{\circ}$, $0^{\circ} \leq \theta_D \leq 45^{\circ}$
 800lx at an operation table

b) Display specifications

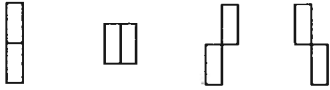
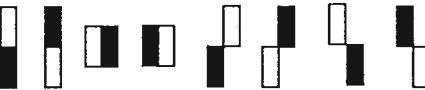
Defect pattern	Condition	Criteria
Line defect	Not allowed	
Bright dots Note1, Note2	Red + Green + Blue	≤ 3 dots
	Green	≤ 1 dot
	Linked 2 dots Note4	0 set
	Linked 3 or more dots	0 set
	Within a circle of $\phi 10\text{mm}$	≤ 1 dot
Dark dots Note1, Note3	Red + Green + Blue	≤ 3 dots
	Linked 2 dots Note4	≤ 1 set
	Linked 3 or more dots	0 set
Between Bright dots and Dark dots	Linked 2 dots Note4	≤ 1 set
Total	Bright dots + Dark dots	≤ 3 dots

Note1: Regardless of bright or intermittent bright, 1/3 or more defects of a dot area is counted as the defect dot.

Note2: Bright dots are measured while the display is black.

Note3: Dark dots are measured while the display is illuminated with red, green and blue.

Note4: The pattern of linked 2 dots is as follows.

 : Bright dot  : Dark dot	Criteria
	0 set
	≤ 1 set
	

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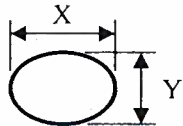
c) Appearance specification

Defect pattern	Condition			Criteria
Other objects 1 (Stains, Dusts, Dent, Bubble, Foreign materials, Dark spot, White spot & Fungus) Note1, Note4	d < 0.2mm			All allowed
	0.2mm ≤ d < 0.4mm			≤ 3 points
	0.4 mm≤ d			0 point
Other objects 2 (Stains & Dusts) Note2, Note4	W ≤ 0.1mm	L < 0.5mm		All allowed
		0.5mm ≤ L < 2.5mm		≤ 3 points
	W > 0.1mm			0 point
	L ≥ 2.5mm			0 point
Touch panel scratch Note2, Note4	W ≤ 0.05mm	L ≤ 2.0mm		All allowed
	W > 0.05mm	L ≤ 2.0mm		0 point
	L > 2.0mm			0 point
Touch panel glass crack Note3	A ≤ 2.0mm	B ≤ 2.0mm	Z ≤ T	All allowed
	A > 2.0mm	B > 0.5mm		0 point
		B ≤ 0.5mm		All allowed
	B > 2.0mm	A > 0.5mm	Z ≤ T	0 point
		A ≤ 0.5mm		All allowed
	C ≤ 5.0mm	D ≤ 1.0mm	Z ≤ T	All allowed
		D > 1.0mm		
	C > 5.0mm	D ≤ 1.0mm	Z ≤ T	All allowed
D > 1.0mm		0 point		

Note1: The shape is like a point.

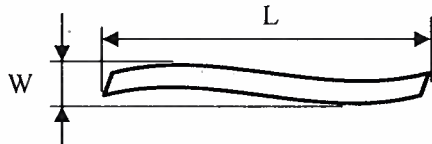
The definition of "d" is as follows.

Average diameter (d) mm: $d = (X+Y)/2$

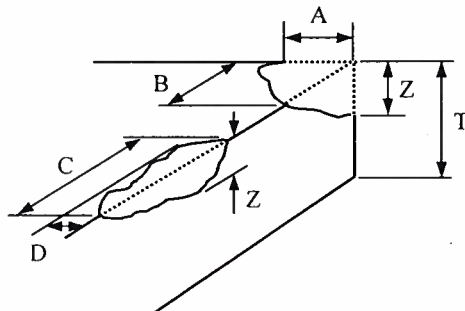


Note2: The shape is like a linear.

The definitions of "W" and "L" are as follows.



Note3: The definitions of "A", "B", "C", "D", "Z" and "T" are as follows.



Note4: Inspection area

53.64 mm (H)×71.52 mm (V) (Display area)

Note5: Clear, unclear Mura and any other bands should be inspected based on NEC's limit samples.

Any question arising out from the shipping products will be discussed between both parties.

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6. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

Condition		Estimated luminance lifetime (MTTF) Note1, Note2, Note3	Unit
Module	25°C (Ambient temperature of LED) Continuous operation, LED current: 20mA	3,400	h

Note1: MTTF is mean time to half-luminance.

Note2: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

Note3: The Luminance life-time is estimated from module Luminance life-time results (n=5) of the representative products. It might be vary with the characteristic of individual LEDs.

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7. PRODUCT INSPECTIONS

The following inspections are carried out on products before shipment

(1) 100% inspection

- Display
- Appearance

(2) Sampling inspection

- White luminance
- Contrast ratio
- Reflection ratio
- Luminance uniformity

8. RELIABILITY TESTS

Test item	Condition	Judgment Note1
High temperature and humidity (Operation)	① $55 \pm 2^{\circ}\text{C}$, RH = 85%, 240 hours ② Display data is black.	No display malfunctions
Heat cycle (Operation)	① $-20 \pm 3^{\circ}\text{C}$...1 hour $70 \pm 3^{\circ}\text{C}$...1 hour ② 50 cycles, 4 hours/cycle ③ Display data is black.	
Thermal shock (Non operation)	① $-30 \pm 3^{\circ}\text{C}$...30 minutes $80 \pm 3^{\circ}\text{C}$...30 minutes ② 100 cycles, 1 hour/cycle ③ Temperature transition time is within 5 minutes.	
Low pressure (Non operation)	① 15kPa ② $-30 \pm 3^{\circ}\text{C}$...24 hours ③ $80 \pm 3^{\circ}\text{C}$...24 hours	
Low pressure (Operation)	① 53.3 kPa ② $-20 \pm 3^{\circ}\text{C}$...24 hours ③ $70 \pm 3^{\circ}\text{C}$...24 hours	
ESD (Operation)	① 150pF, 150Ω, $\pm 10\text{kV}$ ② 3 places on a panel surface ③ 10 times each places at 1 sec interval	
Dust (Operation)	① Sample dust: No. 15 (by JIS-Z8901) ② 15 seconds stir ③ 8 times repeat at 1 hour interval	No display malfunctions No physical damages
Vibration (Operation)	① 30 to 100Hz, 19.6m/s^2 ② 30 minutes/cycle ③ X, Y, Z directions ④ 1 times each directions	
Mechanical shock (Non operation)	① $3,920\text{m/s}^2$, 2.5ms ② $\pm X$, $\pm Y$, $\pm Z$ directions ③ 1 times each directions	

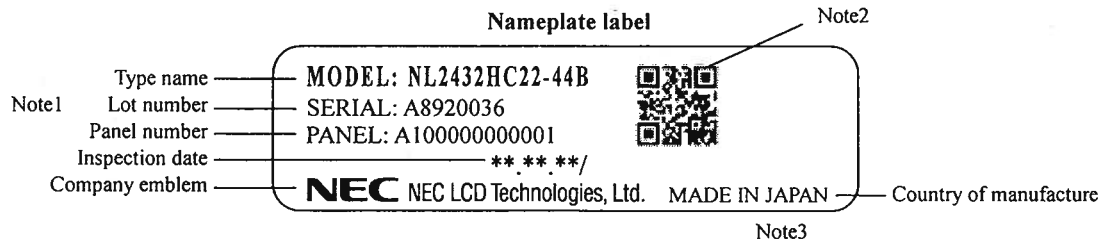
Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect specifications.

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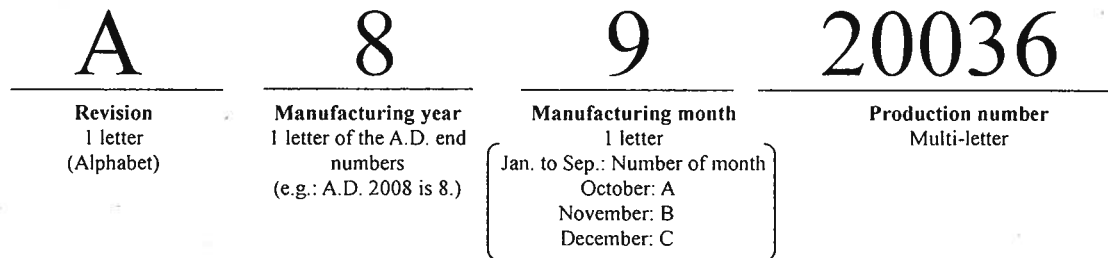
9. MARKING

Name plate label is attached to this product.



Note1: The meaning of lot number

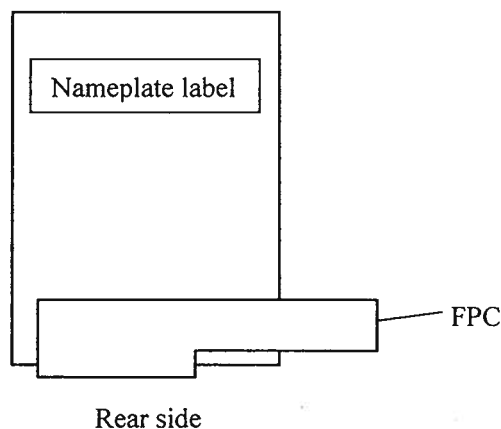
- Example: A8920036



Note2: 2D Barcode: Type name, Lot number and Panel number

Note3: **Do not attach anything such as label and so on, on the nameplate!** As replacing the product, NEC needs the contents of nameplate such as the lot number, inspection date and so on, to identify the warranty period with individual product. If NEC cannot decipher the contents of nameplate, such replacement shall be entitled to charge.

Note4: Label location



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10. PACKING, TRANSPORTATION AND DELIVERY

NEC will pack products to deliver to customer in accordance with NEC's packing specifications, and will deliver products to customer in such a condition that products will not suffer from a damage during transportation. The delivery conditions are as follows.

10.1 PACKING

(1) Inner packing box

80 products are packed as the maximum in an inner packing box (See "10.5 OUTLINE FIGURE FOR PACKING"). The type name and quantity are shown on outside of the inner packing box, either labeling or printing.

(2) Outer packing box

2 inner packing boxes with products are packed in an outer packing box (See "10.5 OUTLINE FIGURE FOR PACKING"). The type name and quantity are shown on outside of the outer packing box, either labeling or printing. In case the outer packing box with products is dropped from a height of 40cm or more, there is a risk of damage to products.

(3) Pallet packing (See "10.5 OUTLINE FIGURE FOR PACKING")

① Transport by aircraft

Inner packing boxes B are tiered on a cardboard pallet. (8 Outer packing boxes × 8 tiers maximum)

② Cardboard sleeve and top cap are attached to packing boxes, and then they are fixed by bands.

The product is shipped by the outer packing box or the pallet packing according to shipment quantity.

10.2 INSPECTION RECORD SHEET

Inspection record sheets are included in an inner packing box with products. It is summarized to a number of products for pass/fail assessment.

10.3 TRANSPORTATION

The product is transported by vehicle, aircraft or ship.

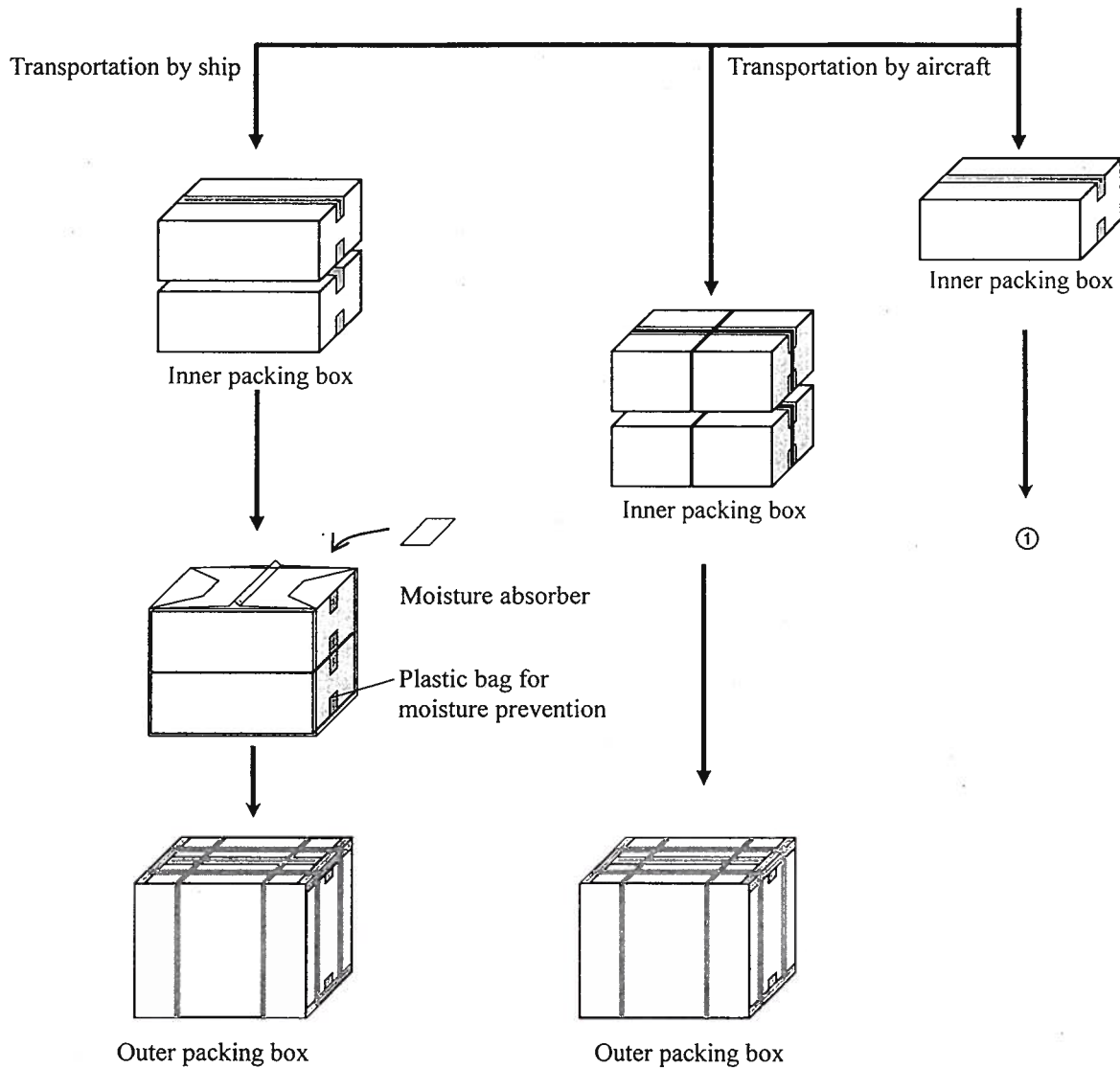
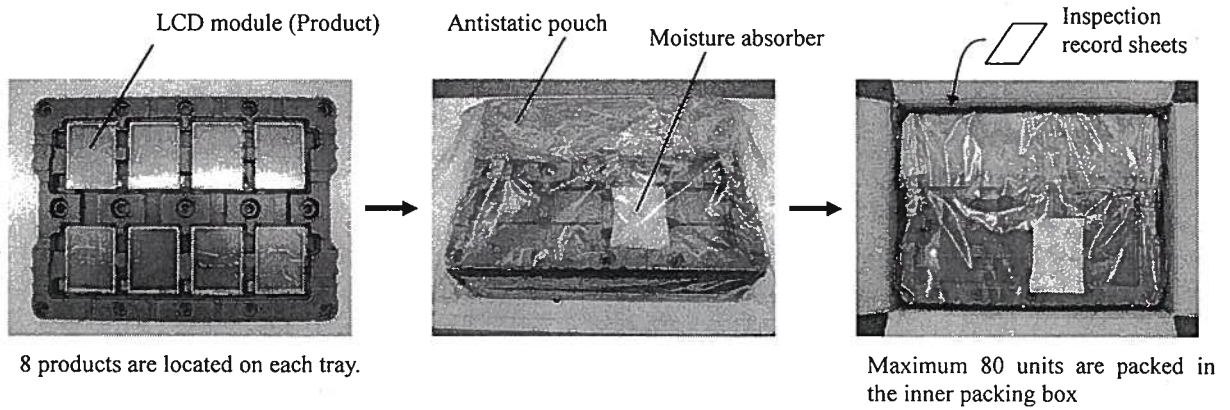
10.4 SIZE AND WEIGHT FOR PACKING BOXES

Parameter	Inner packing box	Outer packing box	Unit
Size	321 (W) × 167 (H) × 421 (D) (typ.)	353 (W) × 382 (H) × 448 (D) (typ.)	mm
Total weight	5.7 (typ.) (with 80 products)	12.6 (typ.) (with 2 inner boxes and 160 products)	kg

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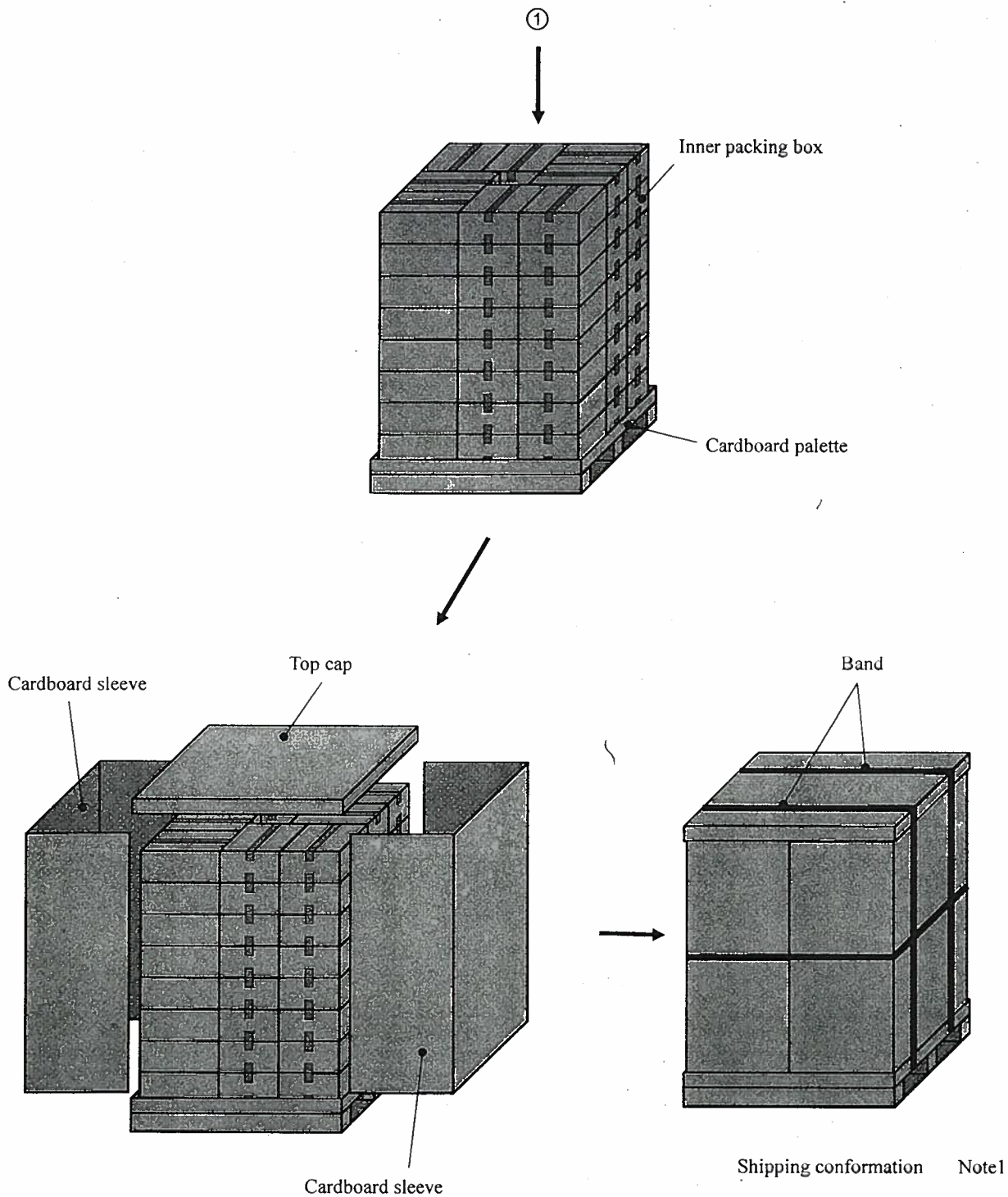
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10.5 OUTLINE FIGURE FOR PACKING



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Note1: Packing specifications

8 Inner packing boxes × 8 tiers maximum

Size: 1,120 (W) × 1,500 (H) × 1,140 (D) mm

Weight: 381kg (with 64 inner packing boxes and 5,120 products)

NEC LCD Technologies, Ltd.

11. PRECAUTIONS

11.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "11.2 CAUTIONS" and "11.3 ATTENTIONS", after understanding these contents!**



This sign has the meaning that customer will be injured by himself or the product will sustain a damage, if customer has wrong operations.



This sign has the meaning that customer will be injured by himself, if customer has wrong operations.

11.2 CAUTIONS



- * Do not touch the working backlight. There is a danger of burn injury.
- * Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: To be not greater $3,920\text{m/s}^2$ and to be not greater 2.5ms)



11.3 ATTENTIONS

11.3.1 Handling of the product

- ① Take hold of both ends without touching the FPC when the product (LCD module) is picked up from the tray.
- ② Do not hook nor pull the FPC in order to avoid any damage.
- ③ When the product is put on the table temporarily, display surface must be placed downward.
- ④ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⑤ The product must be installed and/or handled without any stress such as bends or twist. Bends, twist or any stress to any portion may cause display failures. And also do not put heavy or hard materials on the product.
- ⑥ Do not hit or rub the surface of touch panel with hard materials, because it is easily scratched. (Touch panel pencil-hardness: 3H)
- ⑦ When cleaning the T/P surface, wipe it with a soft dry cloth.
- ⑧ Do not push nor pull the FPC while the product is working.
- ⑨ Do not fold the FPC. When the FPC is folded, pattern disconnection may be caused. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.
- ⑩ When installing the product, do not contact a conductor such as a metal to the FPC excluding the terminal area. There is a risk of short circuit which is caused by breakage of insulation layer of the FPC.
- ⑪ When installing the product, apply the waterproof design to avoid going of water into the product.
- ⑫ Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal for the worst, please wash it out with soap.

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11.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid for dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- ③ Do not operate in high magnetic field. Circuits may be broken down by it.
- ④ This product is not designed as radiation hardened.

11.3.3 Characteristics

The following items are neither defects nor failures.

- ① Response time, luminance and color may be changed by ambient temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking.
- ④ Optical characteristics may be changed depending on input signal timings.
- ⑤ Touch panel film has polarizing characteristic. And the polarizer characteristics differ among products. Therefore, when seeing the displays through the other polarizing material (for example polarizing sunglasses), some displays can not be seen and some displays look different color darker because of polarizer characteristic mismatching between touch panel film and the other polarizing material.

11.3.4 Other

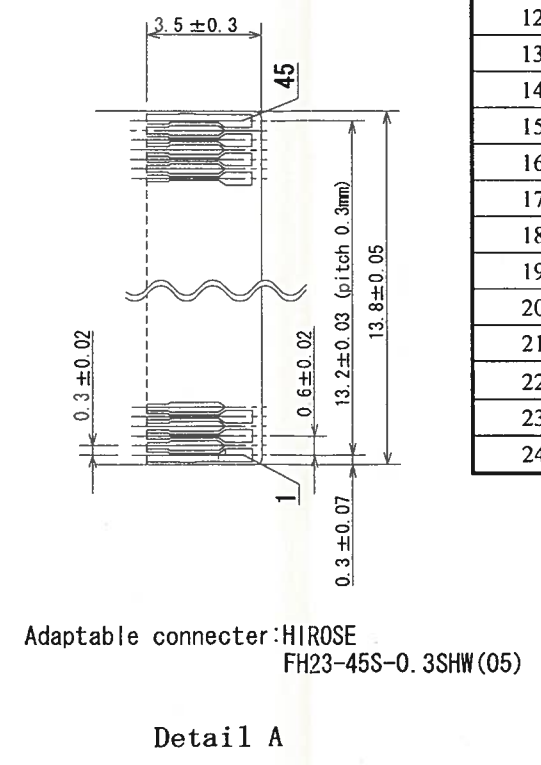
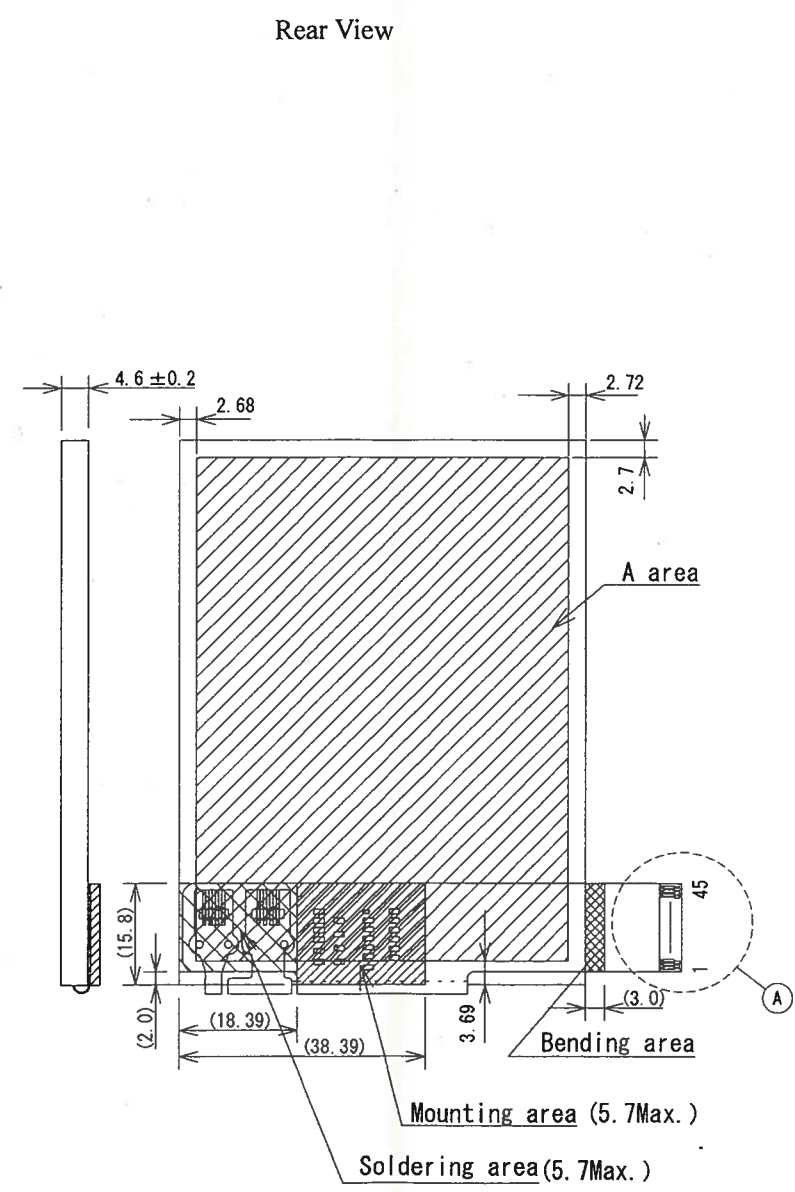
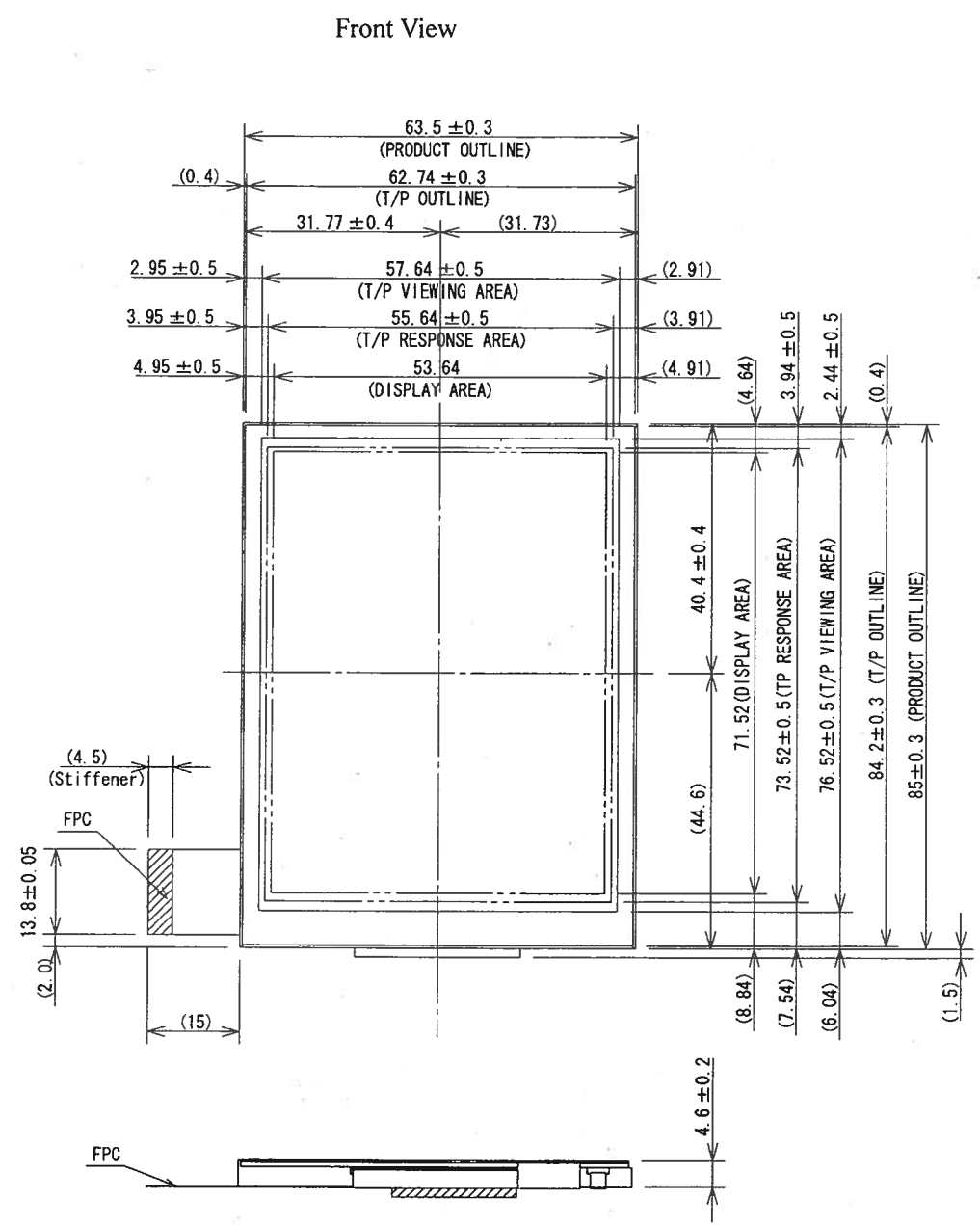
- ① All GND terminals should be used without any non-connected lines.
- ② Do not disassemble the product.
- ③ Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC.
- ④ When installing the product to customer equipment, do not apply any stress to the rear side of the product, FPC, A area, Soldering Area and Mounting Area. If not, it may cause display un-uniformity or LCD panel separation or break down of the product.
- ⑤ The information of China RoHS directive six hazardous substances or elements in this product is as follows.

China RoHS directive six hazardous substances or elements					
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenyls (PBB)	Polybrominated Biphenyl Ethers (PBDE)
○	○	○	○	○	○

Notel: ○ : This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of SJ/T11363-2006 standard regulation.

× : This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of SJ/T11363-2006 standard regulation.

12. OUTLINE DRAWINGS

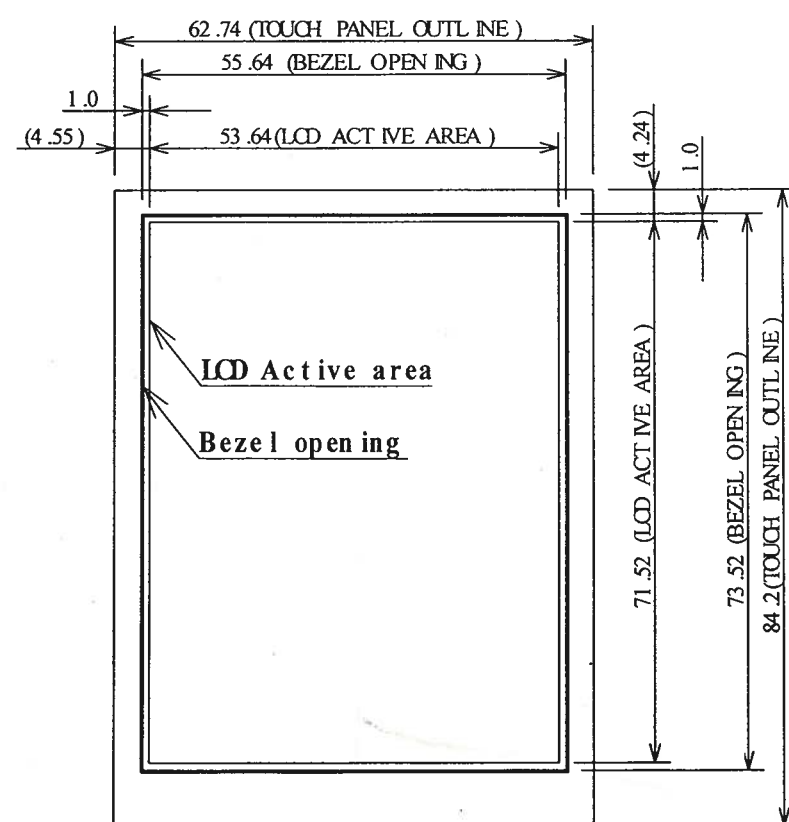
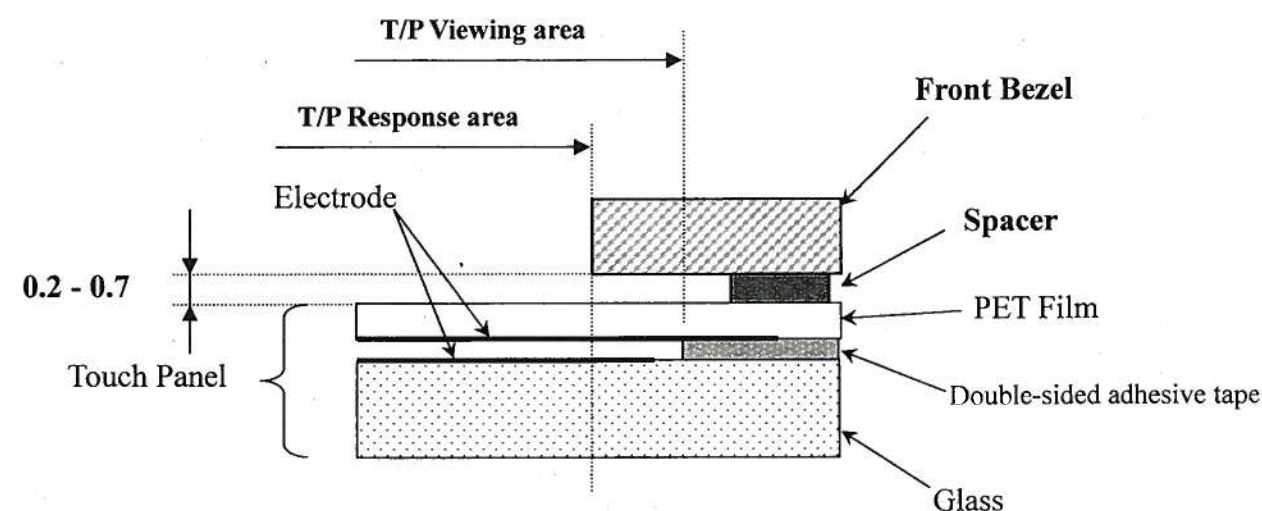


Pin No.	Symbols	Pin No.	Symbols
1	GND	25	R0
2	GND	26	R1
3	VCC	27	R2
4	VCC	28	R3
5	VCC	29	R4
6	VCC	30	R5
7	GND	31	GND
8	/RESET	32	SCL
9	HSYNC	33	SI
10	VSYSN	34	SO
11	CLK	35	/CS
12	GND	36	VCOMIN
13	B0	37	N.C.
14	B1	38	GND
15	B2	39	XL
16	B3	40	YD
17	B4	41	XR
18	B5	42	YU
19	G0	43	GND
20	G1	44	ANODE
21	G2	45	CATHODE
22	G3		
23	G4		
24	G5		

Note1: The values in parentheses are for reference.
Note2: When installing the product to customer equipment, do not apply any stress to the rear side of the product, FPC, A area, Soldering Area and Mounting Area.
If not, it may cause display un-uniformity or LCD panel separation or break down of the product.
Note3: While the product is working, do not contact a conductor such as a metal to the Soldering Area and Mounting Area of the FPC.

Unit: mm

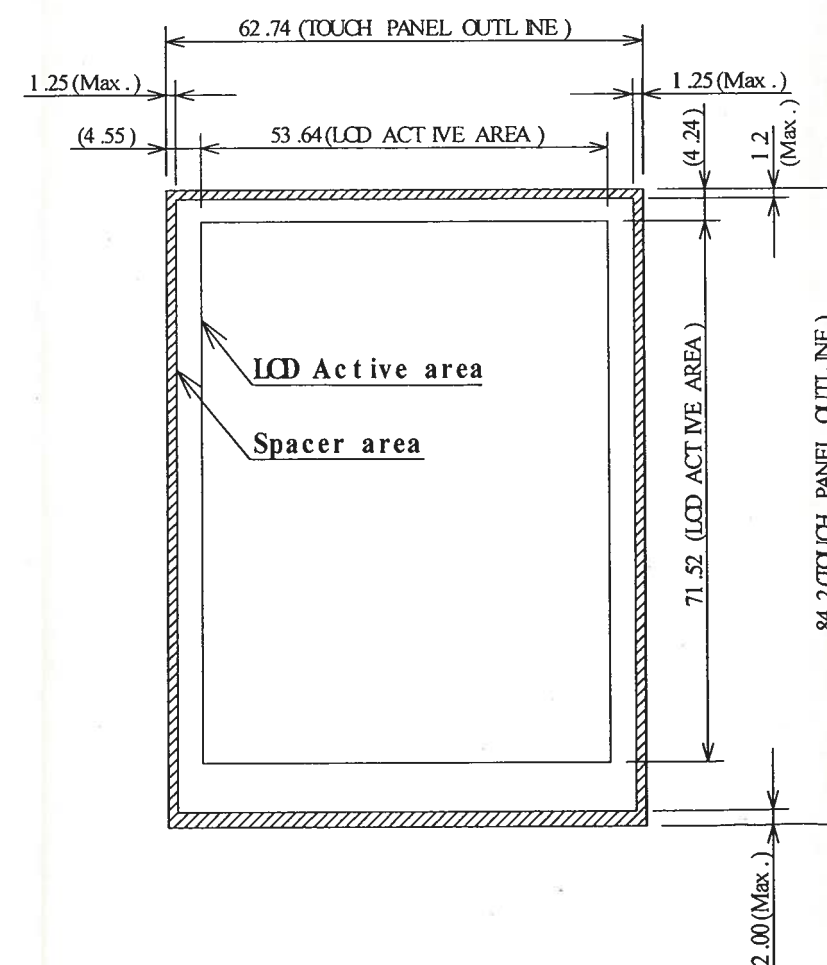
13. RECOMMENDED DESIGN OF FRONT BEZEL



Front Bezel opening design

Design guidance for the front bezel and the spacer

1. Front Bezel opening design
 - a. Please place the front bezel opening to maintain the operation by a stylus pen inside the T/P response area.
 - b. Any pressures in the area between T/P response area and T/P viewing area are prohibited. Please use the appropriate material as the front bezel.
2. Spacer design
 - a. Please put the spacer, a cushion, on the front bezel. Do not use a double-sided adhesive tape because it adheres on the touch panel surface.
 - b. Please position the spacer over the Spacer area to avoid a "short".



Spacer design

Unit: mm

		DDS-SA11110	37/37
REVISION HISTORY			
<i>The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.</i>			
Edition	Prepared date	Revision contents and signature	Issued date
1st edition	Oct. 1, 2008	Revision contents New issue Signature of writer Approved by <u>H. Fukuyoshi</u> Checked by _____ Prepared by <u>E. Katayama</u> H. FUKUYOSHI E. KATAYAMA Signature of acknowledger Module Design Department Product Engineering Department Quality Assurance Department <u>I. HazeYama</u> <u>T. Katoh</u> <u>M. Shimizu</u> I. HAZEYAMA T. KATOH M. SHIMIZU Product Planning Department <u>M. Akiyoshi</u> M. AKIYOSHI	