

PRELIMINARY

NEC NEC LCD Technologies, Ltd.

TFT COLOR LCD MODULE

NL8060BC21-10

**21cm (8.4 Type)
SVGA**

PRELIMINARY DATA SHEET 

DOD-PP-0709 (1st edition)

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starting to design your system.**

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Examples: Computers, office automation equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment, industrial robots, etc.

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Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "**Standard**" unless otherwise specified in this document.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL8060BC21-10 is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing circuit, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

- For industrial use

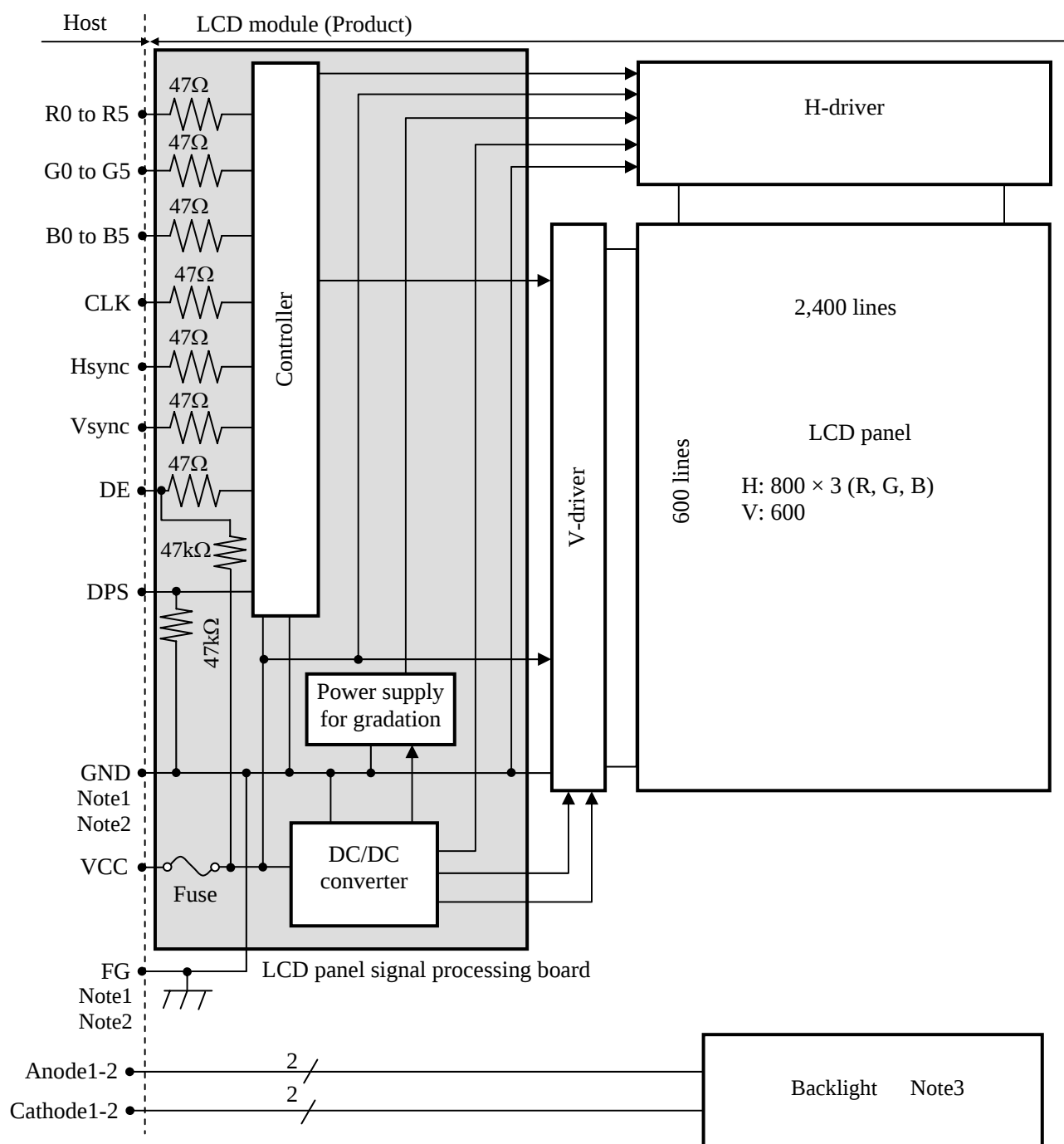
1.3 FEATURES

- High luminance
- High contrast
- Wide viewing angle
- Wide temperature range
- 6-bit digital RGB signals
- Reversible-scan direction
- LED backlight type
- Replaceable lamp holder for backlight

2. GENERAL SPECIFICATIONS

Display area	170.4 (H) × 127.8 (V) mm
Diagonal size of display	21cm (8.4 inches)
Drive system	a-Si TFT active matrix
Display color	262,144 colors
Pixel	800 (H) × 600 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.071 (H) × 0.213 (V) mm
Pixel pitch	0.213 (H) × 0.213 (V) mm
Module size	200.0 (W) × 152.0 (H) × 10.5 (D) mm (typ.)
Weight	(330) g (typ.)
Contrast ratio	(600:1) (typ.)
Viewing angle	At the contrast ratio $\geq 10:1$ • Horizontal: Right side 80° (typ.), Left side 80° (typ.) • Vertical: Up side 80° (typ.), Down side 60° (typ.)
Designed viewing direction	At DPS= Low or open: Normal scan • Viewing direction without image reversal: up side (12 o'clock) • Viewing direction with contrast peak: down side (6 o'clock) • Viewing angle with optimum grayscale ($\gamma \approx 2.2$): normal axis (perpendicular)
Polarizer surface	Clear
Polarizer pencil-hardness	3H (min.) [by JIS K5400]
Color gamut	At LCD panel center 40% (typ.) [against NTSC color space]
Response time	Ton+Toff (10% $\leftrightarrow$ 90%) 25ms (typ.)
Luminance	At IL=50mA (400)cd/m ² (typ.)
Signal system	6-bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE), Horizontal synchronous signal (Hsync), Vertical synchronous signal (Vsync)
Power supply voltage	LCD panel signal processing board: 3.3V or 5.0V
Backlight	LED backlight type: Replaceable part • Lamp holder set: Type No. TBD
Power consumption	At IL=50mA, Checkered flag pattern (3.5) W (typ.)

3. BLOCK DIAGRAM

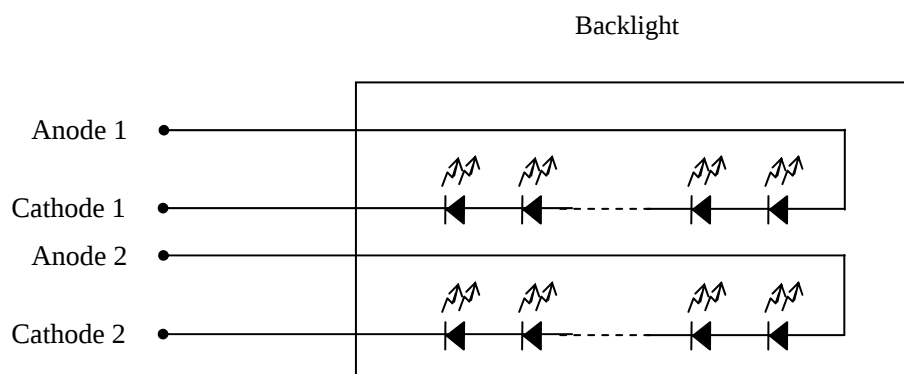


Note1: Relations between GND (Signal ground), FG (Frame ground) and VBLC (Lamp low voltage terminal) in the LCD module are as follows.

GND - FG	Connected
----------	-----------

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds are connected together in customer equipment.

Note3: Backlight in detail



4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification	Unit
Module size	200.0 ± 0.5 (W) × 152.0 ± 0.5 (H) × 10.5 ± 0.5 (D) Note1	mm
Display area	170.4 (H) × 127.8 (V) Note1	mm
Weight	(330) (typ.), TBD (max.)	g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Rating	Unit	Remarks	
Power supply voltage	LCD panel signal processing board	VCC	-0.3 to +6.5	V	-	
Input voltage for signals	Display signals Note1	VD	-0.3 to VCC+0.3	V		
	Function signal Note2	VF				
Backlight	Power dissipation	PD	TBD	W	per one circuit	
	Forward current	IL	TBD	mA	per one circuit	
Storage temperature		Tst	-30 to +80	°C	-	
Operating temperature		Front surface	TopF	-20 to +70	°C	Note3
		Rear surface	TopR	-20 to +70	°C	Note4
Relative humidity Note5		RH	≤ 95	%	Ta ≤ 40°C	
			≤ 85	%	40°C <Ta≤ 50°C	
			≤ 55	%	50°C <Ta≤ 60°C	
			≤ 36	%	60°C <Ta≤ 70°C	
Absolute humidity Note5		AH	≤ 70 Note6	g/m³	Ta> 70°C	

Note1: CLK, Hsync, Vsync, DE, DATA (R0 to R5, G0 to G5 and B0 to B5)

Note2: DPS

Note3: Measured at center of LCD panel surface (including self-heat)

Note4: Measured at center of LCD module's rear shield surface (including self-heat)

Note5: No condensation

Note6: Water amount at Ta= 70°C and RH= 36%

4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

(Ta = 25°C)

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	at VCC = 3.3V
			4.75	5.0	5.25	V	at VCC = 5.0V
Power supply current		ICC	-	360 Note1	480 Note2	mA	at VCC = 3.3V
			-	235 Note1	325 Note2	mA	at VCC = 5.0V
Logic input voltage for display signals	High	VDH	0.7VCC	-	VCC	V	CMOS level
	Low	VDL	0	-	0.3VCC	V	
Input voltage for DPS signal	High	VFH	0.7VCC	-	VCC	V	
	Low	VFL	0	-	0.3VCC	V	

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

4.3.2 Backlight lamp

(Ta=25°C, Note1, Note2)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50.0	55.0	mA	Note3
Forward Voltage	VL	-	23.1	26.6	V	at IL=50 mA

Note1: Please drive with constant current.

Note2: The Luminance uniformity may be changed depending on the current variation between 2 circuits. It is recommended that the current value difference between each circuit is less than 5%.

4.3.3 Power supply voltage ripple

This product works, even if the ripple voltage levels are beyond the permissible values as following the table, but there might be noise on the display image.

Power supply voltage		Ripple voltage (Measure at input terminal of power supply)	Note1	Unit
VCC	3.3V	≤ 100		mVp-p

Note1: The permissible ripple voltage includes spike noise.

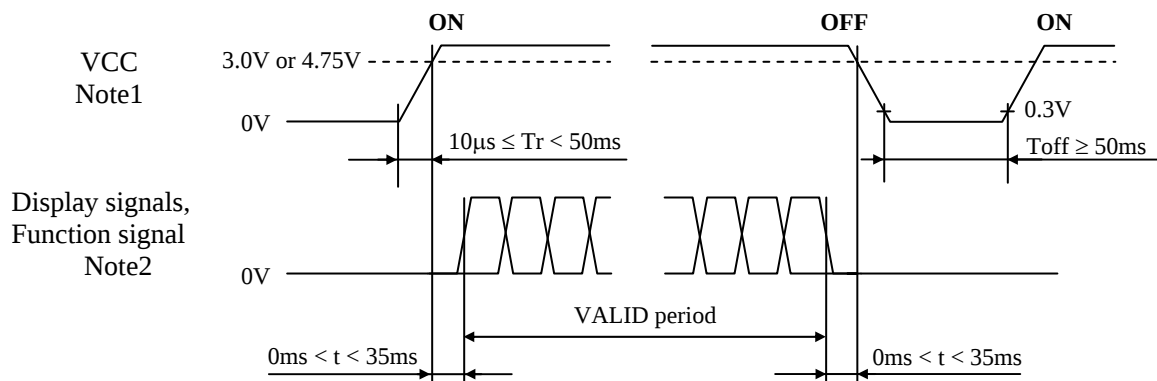
4.3.4 Fuse

Parameter	Fuse		Rating	Fusing current	Remarks
	Type	Supplier			
VCC	FCC16162AB	KAMAYA ELECTRIC Co., Ltd	2.0A	3.2A	Note1
			32V		

Note1: The power supply capacity should be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board

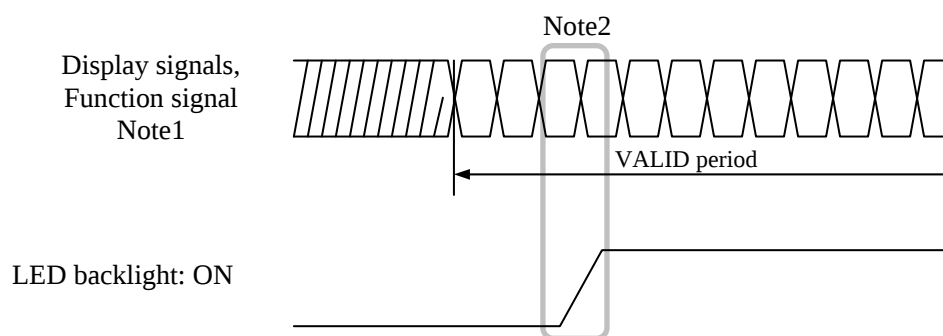


Note1: In terms of voltage variation (voltage drop) while VCC rising edge is below 3.0V in "VCC = 3.3V" or 4.75V in "VCC = 5.0V", a protection circuit may work, and then this product may not work.

Note2: Display signals (CLK, Hsync, Vsync, DE, DATA (R0 to R5, G0 to G5 and B0 to B5)) and function signal (DPS) must be Low or High-impedance, exclude the VALID period (See above sequence diagram), in order to avoid that internal circuits is damaged.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. VCC should be cut when the display and function signals are stopped.

4.4.2 Backlight lighting circuit



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): DF9C-41P-1V (2*) (Hirose Electric Co., Ltd. (HRS))

Adaptable plug: DF9-41S-1V (2*), DF9-41S-1V (3*) (Hirose Electric Co., Ltd. (HRS))

Pin No.	Symbol	Signal	Remarks
1	GND	Ground	Note1
2	CLK	Dot clock	-
3	GND	Ground	Note1
4	Hsync	Horizontal synchronous signal	-
5	Vsync	Vertical synchronous signal	
6	GND	Ground	Note1
7	GND	Ground	
8	GND	Ground	
9	R0	Red data (LSB)	Least significant bit
10	R1	Red data	-
11	R2	Red data	
12	GND	Ground	Note1
13	R3	Red data	-
14	R4	Red data	
15	R5	Red data (MSB)	Most significant bit
16	GND	Ground	Note1
17	GND	Ground	
18	GND	Ground	
19	G0	Green data (LSB)	Least significant bit
20	G1	Green data	-
21	G2	Green data	
22	GND	Ground	Note1
23	G3	Green data	-
24	G4	Green data	
25	G5	Green data (MSB)	Most significant bit
26	GND	Ground	Note1
27	GND	Ground	
28	GND	Ground	
29	B0	Blue data (LSB)	Least significant bit
30	B1	Blue data	-
31	B2	Blue data	
32	GND	Ground	Note1
33	B3	Blue data	-
34	B4	Blue data	
35	B5	Blue data (MSB)	Most significant bit
36	GND	Ground	Note1
37	DE	Selection of DE / Fixed mode	High or Open: Fixed mode Data enable signal: DE mode
38	N.C.	-	Keep this pin Open.
39	VCC	Power supply	Note1
40	VCC	Power supply	
41	DPS	Selection of scan direction	High: Reverse scan Low or Open: Normal scan Note2

Note1: All VCC and GND terminals should be used without any non-connected lines.

Note2: See "4.8 SCANNING DIRECTIONS".

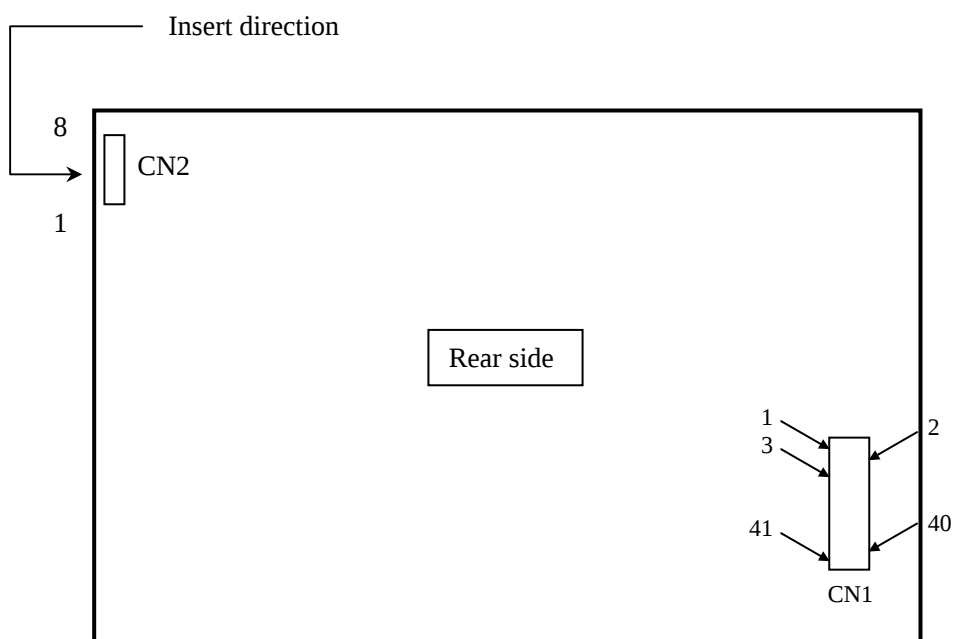
4.5.2 Backlight lamp

CN2 plug (LCD module side): SM08B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)

Adaptable socket: SHR-8V-S, SHR-8V-S-B (J.S.T. Mfg. Co., Ltd.)

Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	N.C.	-	Keep this pin Open.
6	N.C.	-	Keep this pin Open.
7	N.C.	-	Keep this pin Open.
8	N.C.	-	Keep this pin Open.

4.5.3 Positions of plug and socket



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display in equivalent to 262,144 colors in 64 gray scales. Also the relation between display colors and input data signals is as the following table.

Display colors		Data signal (0: Low level, 1: High level)																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0		1	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑				:					:						:			
	↓				:					:						:			
	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Green gray scale		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	↑				:					:						:			
	↓				:					:						:			
Blue gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑				:					:						:			
	↓				:					:						:			
	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
Blue gray scale		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	
	Blue	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1

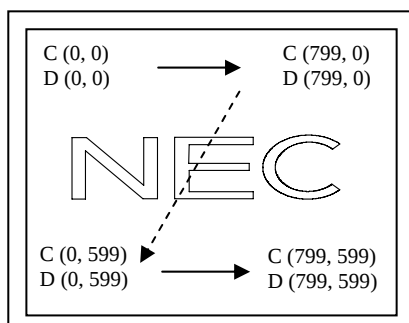
4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0, 0)						
R	G	B				
C(0, 0)	C(1, 0)	• • •	C(X, 0)	• • •	C(798, 0)	C(799, 0)
C(0, 1)	C(1, 1)	• • •	C(X, 1)	• • •	C(798, 1)	C(799, 1)
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(798, Y)	C(799, Y)
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
C(0, 598)	C(1, 598)	• • •	C(X, 598)	• • •	C(798, 598)	C(799, 598)
C(0, 599)	C(1, 599)	• • •	C(X, 599)	• • •	C(798, 599)	C(799, 599)

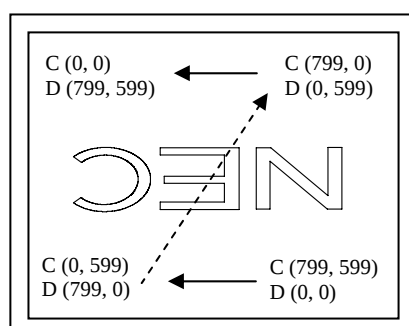
4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.



Note1

Figure1. Normal scan (DPS: Low or Open)



Note1

Figure2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

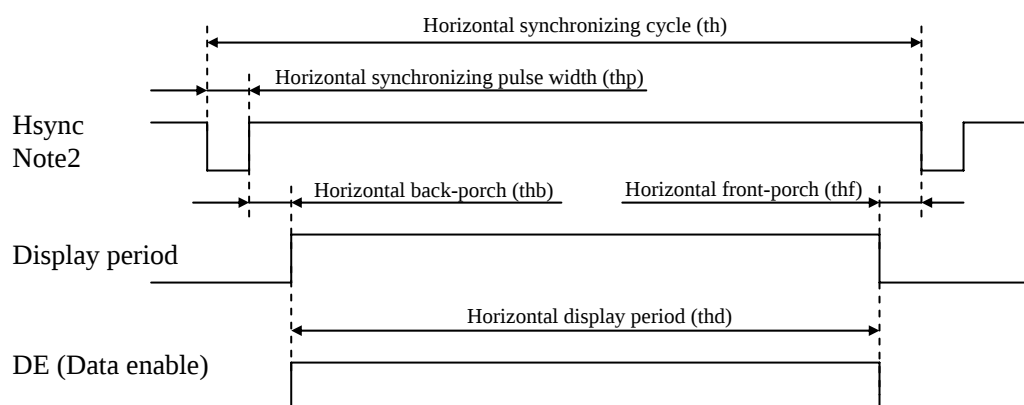
D (X, Y): The data number of input signal for LCD panel signal processing board

4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

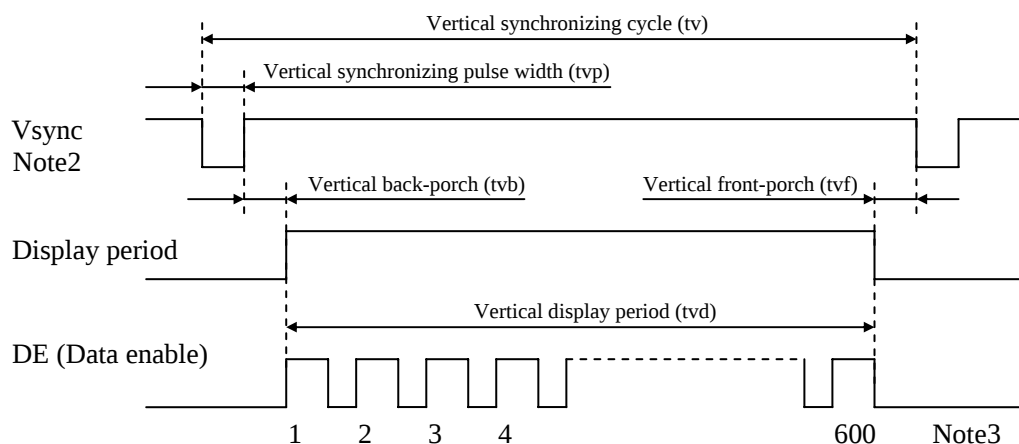
- Horizontal signal

Note1



- Vertical signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: Fixed mode cannot be used while working of DE mode.

Note3: See "4.9.3 Input signal timing chart" for numeration of pulse.

4.9.2 Timing characteristics

(a) Fixed mode

(Note1)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	34.0	38.362	40.0	MHz	26.067ns (typ.)
	Duty		tcd	0.4	0.5	0.6	-	-
	Rise time, Fall time		tcrf	-	-	10	ns	
DATA (R0-R5) (G0-G5) (B0-B5)	CLK-DATA	Setup time	tds	3	-	-	ns	-
		Hold time	tdh	5	-	-	ns	
	Rise time, Fall time		tdrf	-	-	10	ns	
Hsync	Cycle		th	24.0	26.693	30.1	μs	37.463kHz (typ.)
				1,024			CLK	-
	Display period		thd	800			CLK	
	Front-porch		thf	24			CLK	
	Pulse width		thp	12	72	-	CLK	
	Back-porch		thb	-	128	188	CLK	
	Total of pulse width and back-porch		thp + thb	200			CLK	Note2
	CLK- Hsync	Setup time	ths	3	-	-	ns	-
		Hold time	thh	5	-	-	ns	
	Rise time, Fall time		thrf	-	-	10	ns	
Vsync	Cycle		tv	16.1	16.683	17.2	ms	59.94Hz (typ.)
				625			H	-
	Display period		tvd	600			H	
	Front-porch		tvf	1			H	
	Pulse width		tvp	1	2	-	H	
	Back-porch		tvb	-	22	23	H	
	Total of pulse width and back-porch		tvp + tvb	24			H	Note2
	Hsync-Vsync	Setup time	tvhs	3	-	-	ns	-
		Hold time	tvhh	5	-	-	ns	
	Rise time, Fall time		tvrf	-	-	10	ns	

Note1: Definition of parameters is as follows.

tc = 1CLK, tcd = tch/tc, th = 1H

Note2: Keep tvp + tvb and thp + thb within the table. If it is out of specification, display position will be shifted to right/left side or up/down.

(b) DE mode

(Note1, Note2, Note3)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	34.0	38.362	40.0	MHz	26.067 ns (typ.)
	Duty		tcd	0.4	0.5	0.6	-	-
	Rise time, Fall time		tcrf	-	-	10	ns	
DATA (R0-R5) (G0-G5) (B0-B5)	CLK-DATA	Setup time	tds	3	-	-	ns	-
		Hold time	tdh	5	-	-	ns	
	Rise time, Fall time		tdrf	-	-	10	ns	
DE	Horizontal	Cycle	th	24.0	26.693	30.1	μs	37.463 kHz (typ.)
				-	1024	-	CLK	-
		Display period	thd	800			CLK	
	Vertical (One frame)	Cycle	tv	16.1	16.683	17.2	ms	59.94 Hz (typ.)
				-	625	-	H	-
		Display period	tvd	600			H	
	CLK-DE	Setup time	tdes	3	-	-	ns	-
		Hold time	tdeh	5	-	-	ns	
	Rise time, Fall time		tderf	-	-	10	ns	

Note1: Definition of parameters is as follows.

tc = 1CLK, tcd = tch/tc, th = 1H

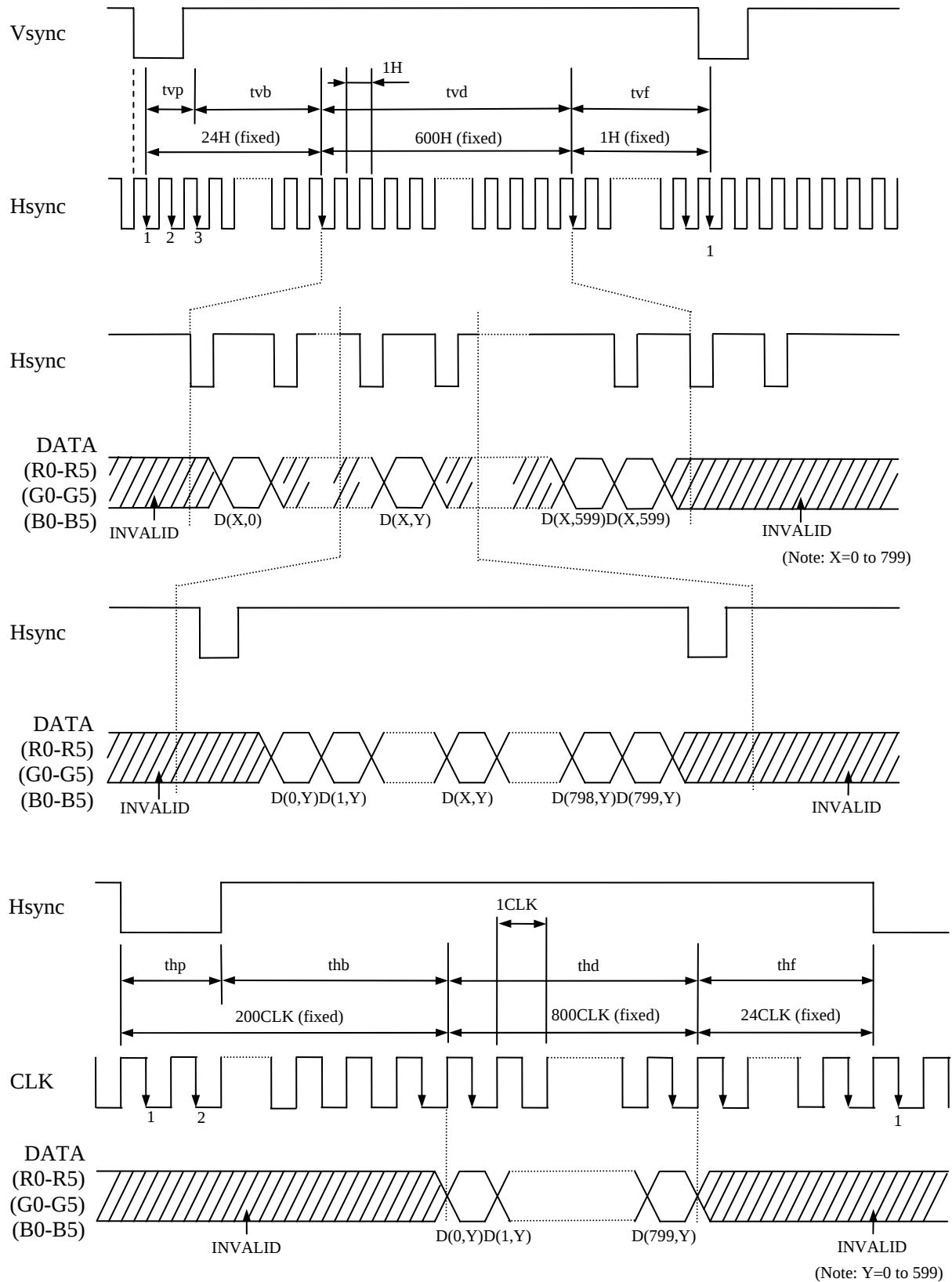
Note2: Hsync signal (Pin No.4 of CN1) and Vsync signal (Pin No.5 of CN1) are not used inside the product at DE mode.

Do not keep pin open to avoid noise problem.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

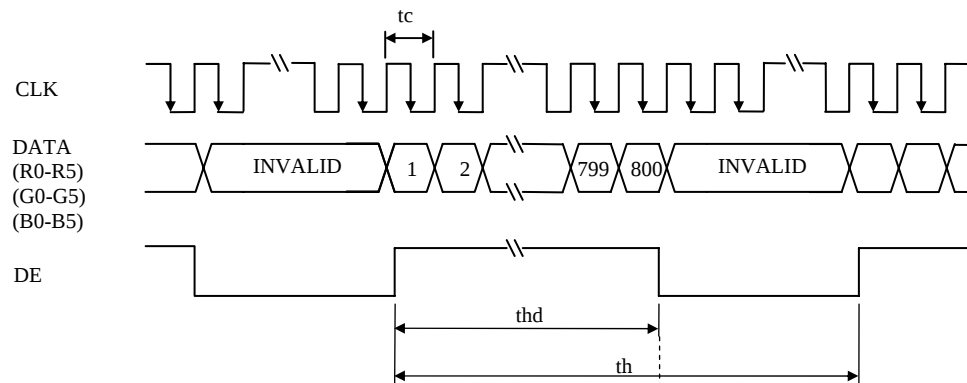
4.9.3 Input signal timing chart

(a) Fixed mode

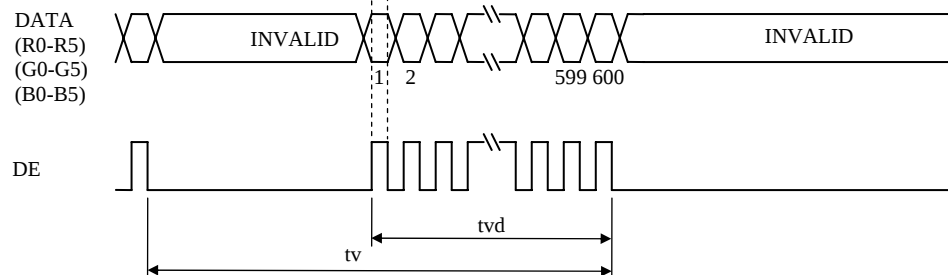


(b) DE mode

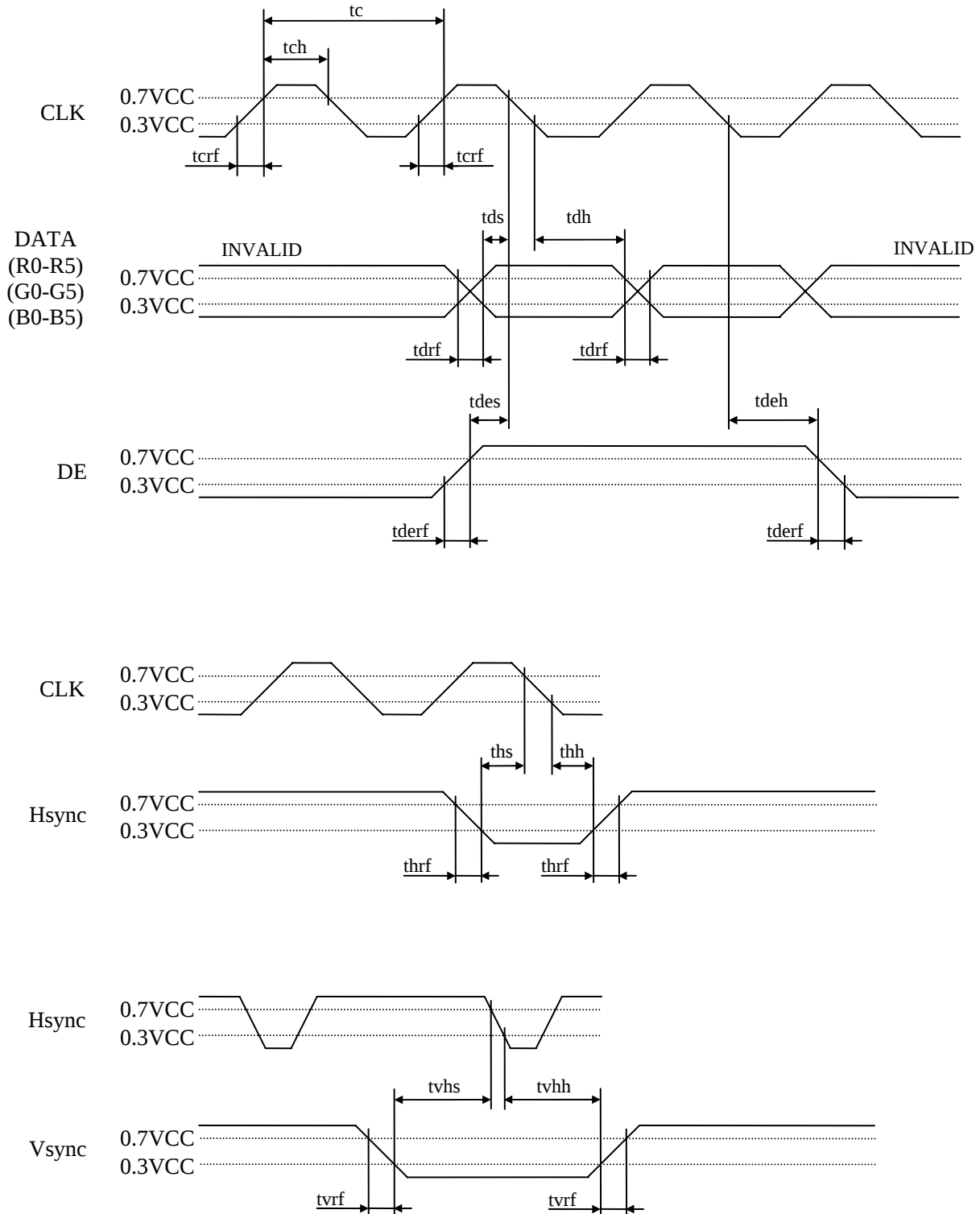
Horizontal timing



Vertical timing



(c) Common item of Fixed mode and DE mode



4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

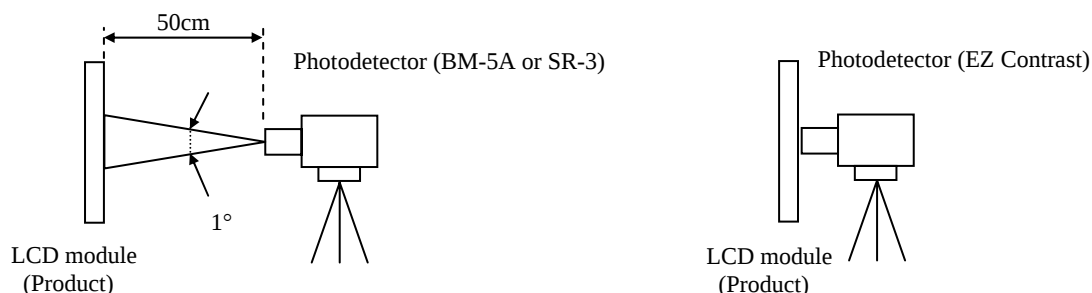
Parameter	Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance	White at center $\theta_R = 0^\circ, \theta_L = 0^\circ, \theta_U = 0^\circ, \theta_D = 0^\circ$	L	TBD	(400)	-	cd/m ²	BM-5A	-
Contrast ratio	White/Black at center $\theta_R = 0^\circ, \theta_L = 0^\circ, \theta_U = 0^\circ, \theta_D = 0^\circ$	CR	TBD	(600)	-	-	BM-5A	Note3
Luminance uniformity	White $\theta_R = 0^\circ, \theta_L = 0^\circ, \theta_U = 0^\circ, \theta_D = 0^\circ$	LU	-	1.25	1.4	-	BM-5A	Note4
Chromaticity	White x coordinate	W _x	TBD	TBD	TBD	-	SR-3	Note5
	White y coordinate	W _y	TBD	TBD	TBD	-		
	Red x coordinate	R _x	-	TBD	-	-		
	Red y coordinate	R _y	-	TBD	-	-		
	Green x coordinate	G _x	-	TBD	-	-		
	Green y coordinate	G _y	-	TBD	-	-		
Blue	x coordinate	B _x	-	TBD	-	-	SR-3	Note5
	y coordinate	B _y	-	TBD	-	-		
Color gamut	$\theta_R = 0^\circ, \theta_L = 0^\circ, \theta_U = 0^\circ, \theta_D = 0^\circ$ at center, against NTSC color space	C	TBD	40	-	%	BM-5A	Note6 Note7
Response time	White to Black	Ton	-	6	15	ms		
	Black to White	Toff	-	19	47	ms	EZ Contrast	Note8
Viewing angle	Right $\theta_U = 0^\circ, \theta_D = 0^\circ, CR \geq 10$	θ_R	70	80	-	°		
	Left $\theta_U = 0^\circ, \theta_D = 0^\circ, CR \geq 10$	θ_L	70	80	-	°		
	Up $\theta_R = 0^\circ, \theta_L = 0^\circ, CR \geq 10$	θ_U	70	80	-	°		
	Down $\theta_R = 0^\circ, \theta_L = 0^\circ, CR \geq 10$	θ_D	50	60	-	°		

Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50mA, Display mode: SVGA, Horizontal cycle= 1/37.463kHz,
Vertical cycle= 1/59.94Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation after 20minutes from working the product, in the dark room. Also measurement methods are as follows.



Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= TBD °C

Note7: See "4.10.4 Definition of response times".

Note8: See "4.10.5 Definition of viewing angles".

4.10.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

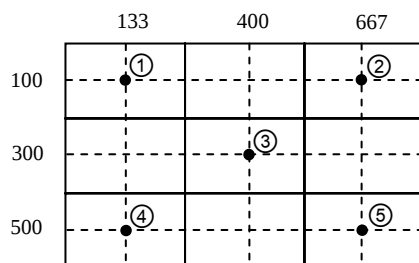
$$\text{Contrast ratio (CR)} = \frac{\text{Luminance of white screen}}{\text{Luminance of black screen}}$$

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

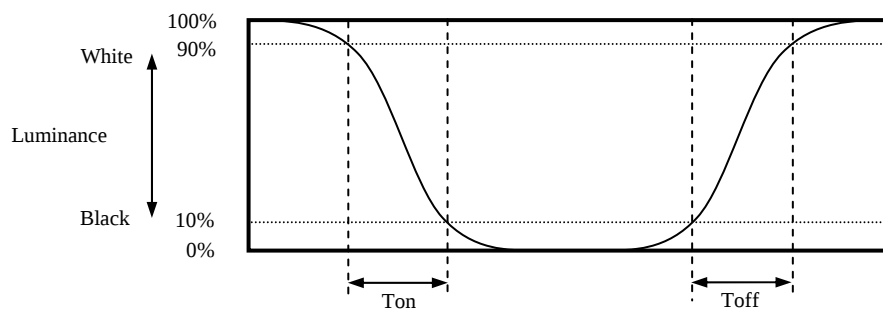
$$\text{Luminance uniformity (LU)} = \frac{\text{Maximum luminance from ① to ⑤}}{\text{Minimum luminance from ① to ⑤}}$$

The luminance is measured at near the 5 points shown below.

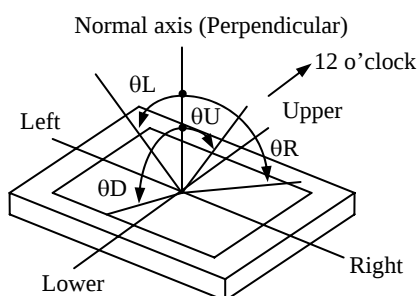


4.10.4 Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles



5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

Condition		Expected luminance lifetime Note1, Note2	Unit
LED elementary substance	25°C (Ambient temperature of LED) Continuous operation, IL=50mA	70,000	h
	70°C (Ambient temperature of LED) Continuous operation, IL=50mA	60,000	h

Note1: Expected luminance lifetime is not the value for LCD module but the value for LED elementary substance.

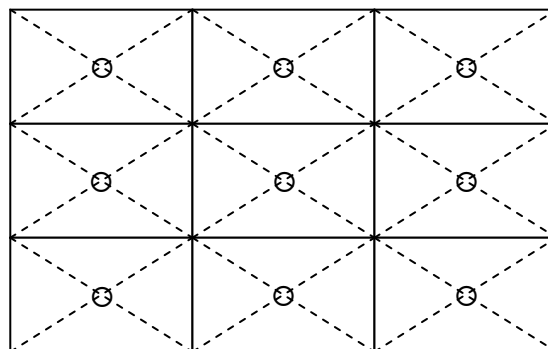
Note2: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

6. RELIABILITY TESTS

Test item	Condition	Judgment	Note1
High temperature and humidity (Operation)	① $60 \pm 2^{\circ}\text{C}$, RH= 90%, 240hours ② Display data is black.	No display malfunctions	
High temperature (Operation)	① $70 \pm 3^{\circ}\text{C}$, 240hours ② Display data is black.		
Heat cycle (Operation)	① $-20 \pm 3^{\circ}\text{C} \dots 1\text{hour}$ $70 \pm 3^{\circ}\text{C} \dots 1\text{hour}$ ② 50cycles, 4 hours/cycle ③ Display data is black.		
Thermal shock (Non operation)	① $-30 \pm 3^{\circ}\text{C} \dots 30\text{minutes}$ $80 \pm 3^{\circ}\text{C} \dots 30\text{minutes}$ ② 100cycles, 1hour/cycle ③ Temperature transition time is within 5 minutes.		
ESD (Operation)	① 150pF, 150Ω, $\pm 10\text{kV}$ ② 9 places on a panel surface Note2 ③ 10 times each places at 1 sec interval		
Dust (Operation)	① Sample dust: No. 15 (by JIS-Z8901)) ② 15 seconds stir ③ 8 times repeat at 1 hour interval	No display malfunctions No physical damages	
Vibration (Non operation)	① 5 to 100Hz, 19.6m/s^2 ② 1 minute/cycle ③ X, Y, Z directions ④ 120 times each directions		
Mechanical shock (Non operation)	① 539m/s^2 , 11ms ② $\pm X$, $\pm Y$, $\pm Z$ directions ③ 5 times each directions		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS", after understanding these contents!**



This sign has the meaning that customer will be injured by personnel or the product will sustain a damage, if customer has wrong operations.



This sign has the meaning that customer will be injured by personnel, if customer has wrong operations.

7.2 CAUTIONS



*** Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: To be not greater 539m/s² and to be not greater 11ms, Pressure: To be not greater 19.6 N (φ16mm jig))**

7.3 ATTENTIONS



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- ③ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ④ The torque for product mounting screws must never exceed 0.294N·m. Higher torque might result in distortion of the bezel.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑥ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ⑦ Do not push nor pull the interface connectors while the product is working.
- ⑧ When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ⑨ Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal for the worst, please wash it out with soap.

7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- ③ Do not operate in high magnetic field. Circuit boards may be broken down by it.
- ④ This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

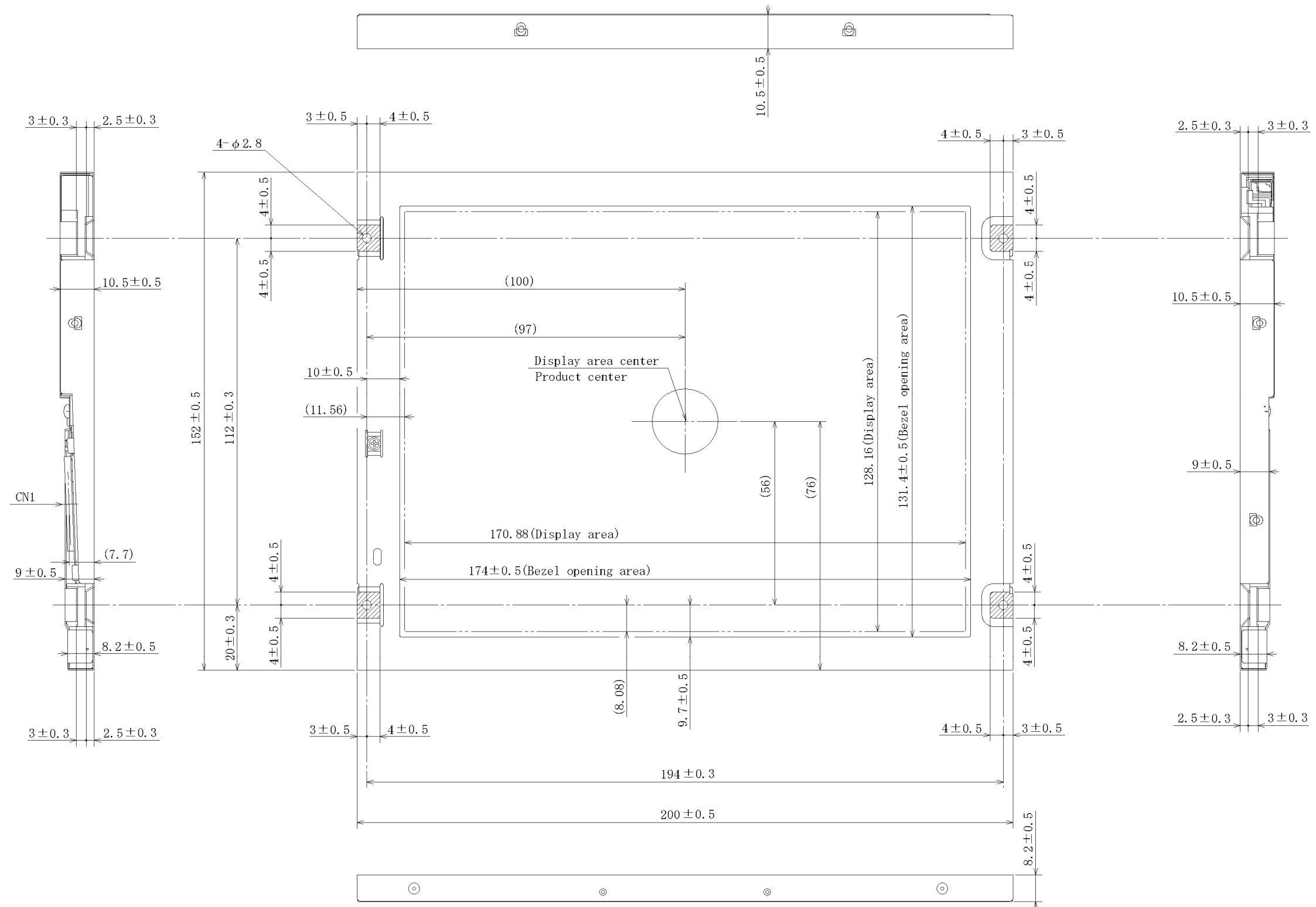
- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- ④ The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Other

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing LED holder.
- ④ Pay attention not to insert foreign materials inside of the product, when using tapping screws.
- ⑤ Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC for repair and so on.

8. OUTLINE DRAWINGS

8.1 FRONT VIEW



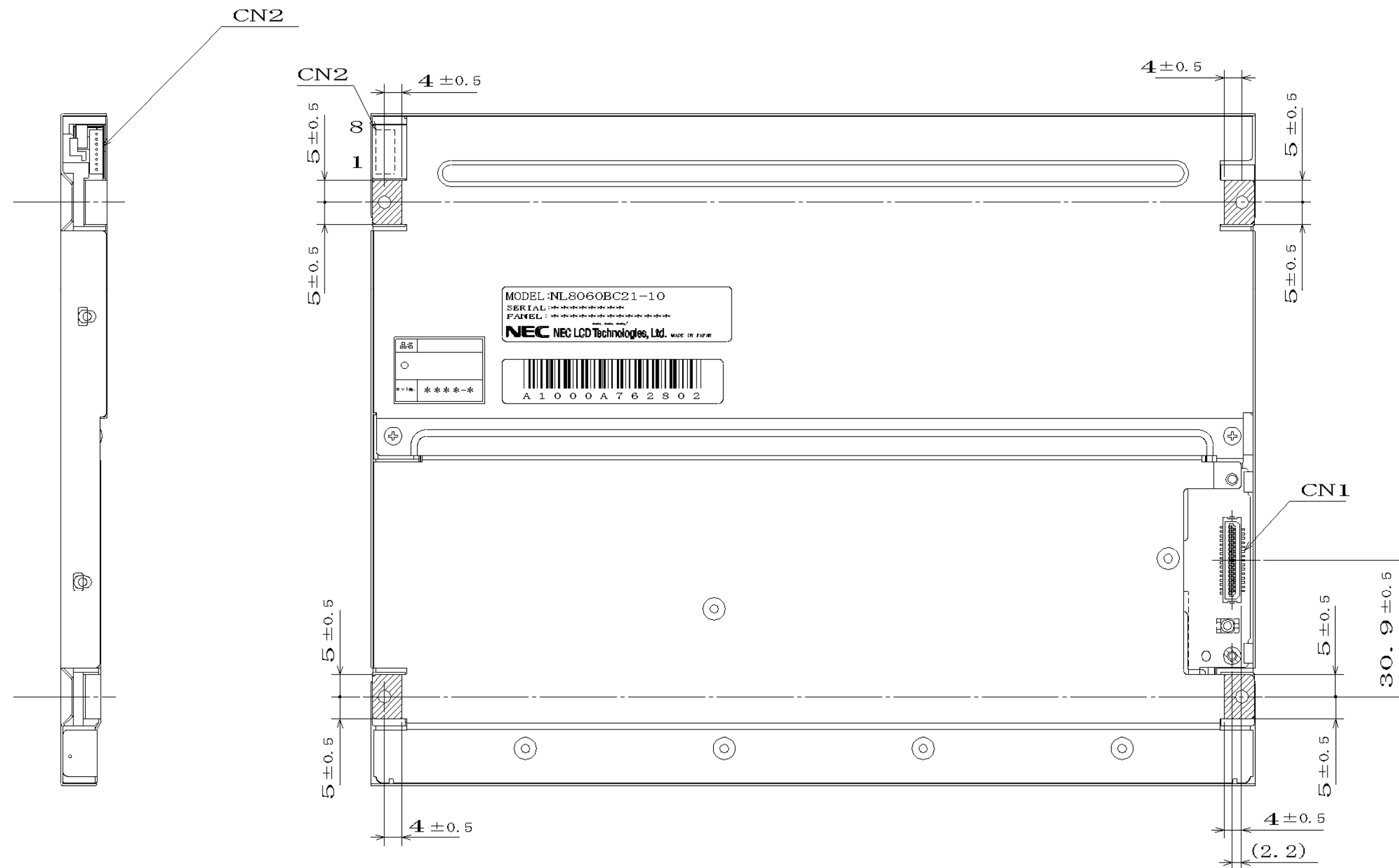
Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.294N·m.

Note3:  Mounting hole portions (4 pieces)

Unit: mm

8.2 REAR VIEW



Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.294N·m.

Note3: Mounting hole portions (4 pieces)

Unit: mm

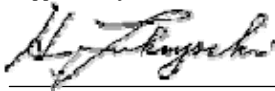
PRELIMINARY

NEC NEC LCD Technologies, Ltd.

NL8060BC21-10

REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Revision contents and signature
1st edition	DOD-PP-0709	Jan. 8, 2009	Revision contents New issue Signature of writer Approved by  _____ H.FUKUYOSHI Checked by _____ Prepared by A. Kumano _____ A.KUMANO